

XRT Timeline to be uploaded on 2024/05/25

Period: 2024/05/25 10:57:00 - 2024/05/30 10:19:00

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Normal mode

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XOB #1CC2: HOP361 - High cadence (8s thin-Be only) 384x384 at 1064 1048												
Term	Pointing (x, y)							Comment				
05/25 11:38:00 - 05/25 17:40:30	Track (601.2, -272.0) ^{© 05/25 11:07:00}	# OP start + 10min. AR 13685 observations.										
PROG= 09 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 22 250-time(s) 8.0sec												
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5795)												
Term	Pointing (x, y)							Comment				
05/25 18:20:30 - 05/25 18:27:24	Fixed (0.0, 0.0)	synoptic, shifted 17.5 min										
05/26 06:03:00 - 05/26 06:10:30	Fixed (0.0, 0.0)	synoptic, shifted 1.0 min + HOP 349										
PROG= 19 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 26 1-time(s) 2.0sec												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 99 1-time(s) 2.0sec												
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 83 1-time(s) 2.0sec												
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	32ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 23 1-time(s) 4.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs 1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs 1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 41 1-time(s) 2.0sec												
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 17 1-time(s) 2.0sec												
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	250ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	8.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 33 1-time(s) 2.0sec												
	med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 75 1-time(s) 2.0sec												
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1BBA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with												
Term	Pointing (x, y)							Comment				
05/25 18:30:30 - 05/26 01:51:30	Track (647.8, -273.5) ^{© 05/25 18:27:30}	# AR obs.										
PROG= 07 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Subr= 2 5-time(s) 2.0sec												
└─ Seqn= 47 1-time(s) 2.0sec												
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs 1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 96	4-time(s)	120.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D09: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[5/181/1443], thin-Be[16/512/3897] with 512x512 G-band+Leak - 90min cad) + CME wat

Term	Pointing (x, y)	Comment
05/26 02:22:00 - 05/26 05:59:54	Fixed (0.0, 0.0)	synoptic, shifted 1.0 min + HOP 349
PROG= 18 Inf.-time(s)		
Subr= 1 1-time(s) 600.0sec		
Seqn= 55 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh	close Safe Norm 2ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 98 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open	close Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open	close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 79 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open	close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open	close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open	close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 30 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 7-time(s) 600.0sec		
Seqn= 8 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 74 1-time(s) 2.0sec		
med-Be/Open	med-Be/Open	close Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
med-Be/Open	med-Be/Open	close Safe Norm 2.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Seqn= 6 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open	close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open	Al-poly/Open	close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 29 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh	close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 250ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

* * * * * **Flare mode** * * * * *

XOB #1D10: Flare - multifilter 5 sec cadence (Be/thin, Be/med), AEC 3, 384x384

Term	Pointing (x, y)	Comment
05/25 11:38:00 - 05/25 17:40:30	Track (601.2, -272.0) @ 05/25 11:07:00	# OP start + 10min. AR 13685 observations.
PROG= 15 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 9 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 49 255-time(s) 5.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 8ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
med-Be/Open	Open/thick-Al	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G

Term	Pointing (x, y)	Comment
05/25 18:30:30 - 05/26 01:51:30	Track (647.8, -273.5) @ 05/25 18:27:30	# AR obs.
05/26 02:22:00 - 05/26 05:59:54	Fixed (0.0, 0.0)	synoptic, shifted 1.0 min + HOP 349
PROG= 14 30-time(s)		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 73 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
med-Be/Open	Open/thick-Al	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec

Seqn= 87	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

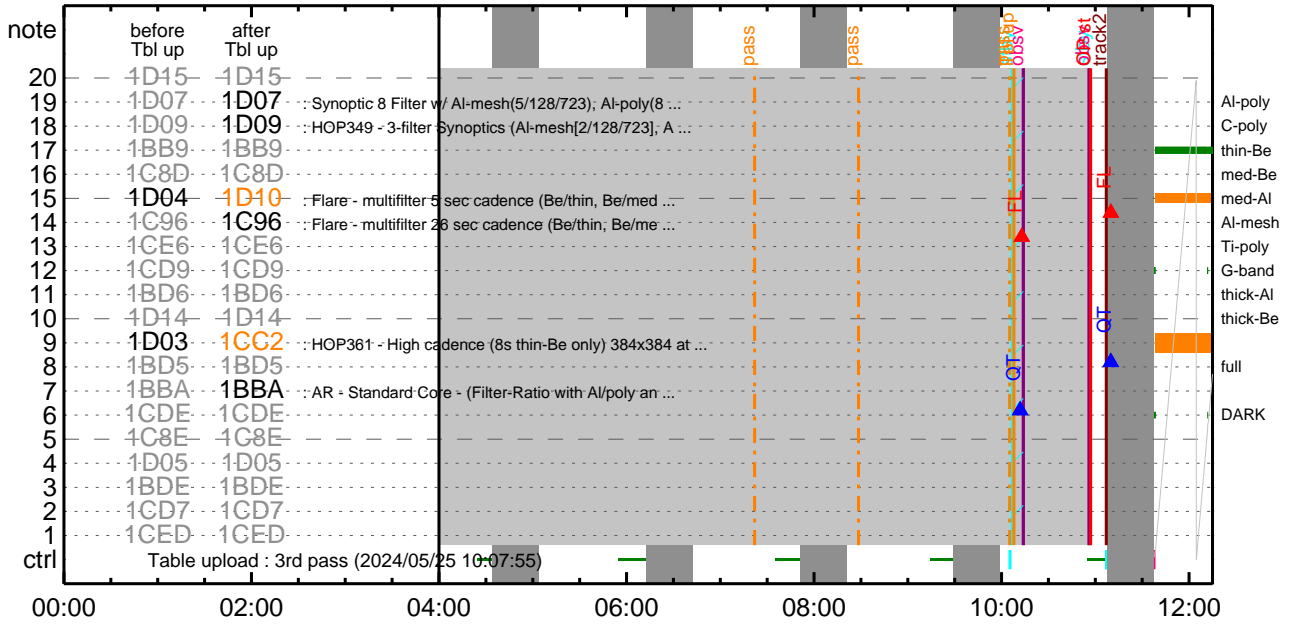
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Flare Detection

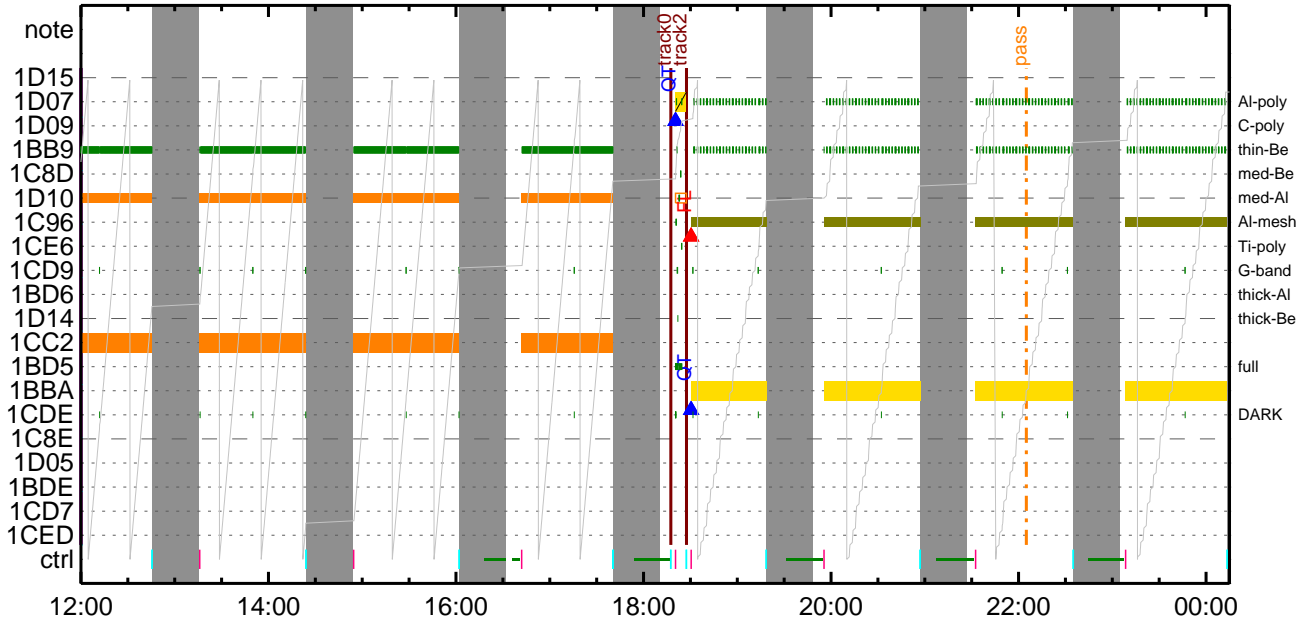
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FLD Patrol												
Term	Pointing (x, y)							Comment				
05/25 10:08:55 - 05/25 18:17:48	cannot be identified											
05/25 18:27:48 - 05/26 06:00:18	Track (647.8, -273.5) @ 05/25 18:27:30 # AR obs.											
AI-poly/Open	AI-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

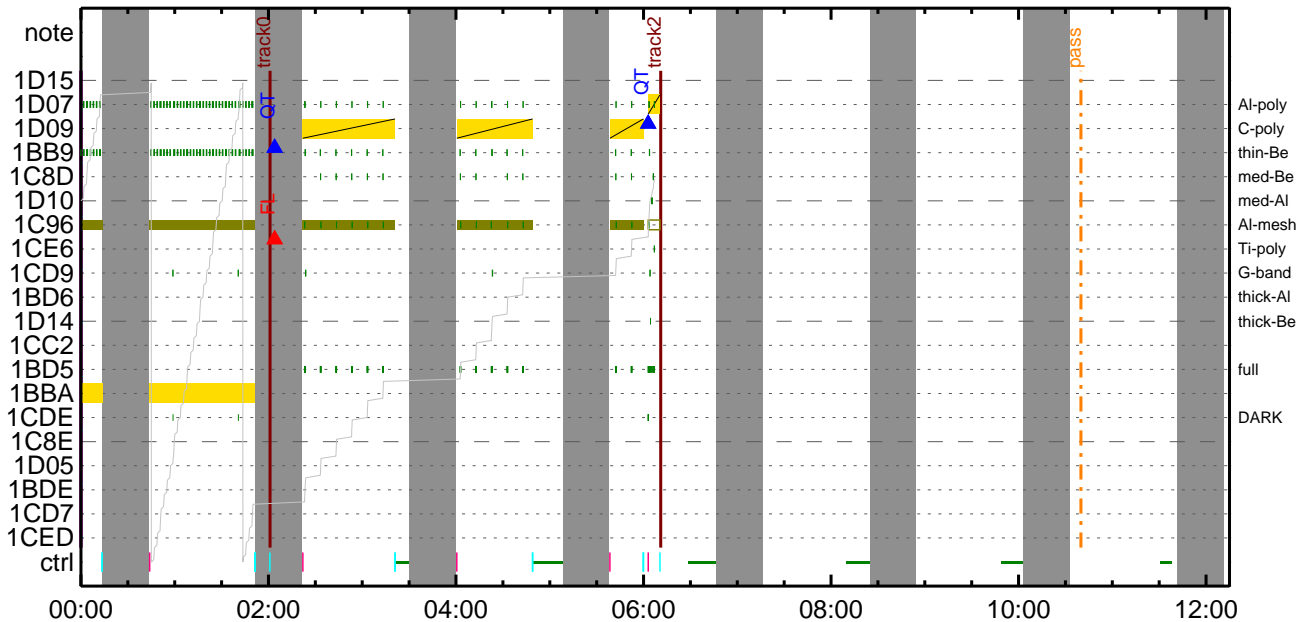
CMDI #0843 2024/05/25



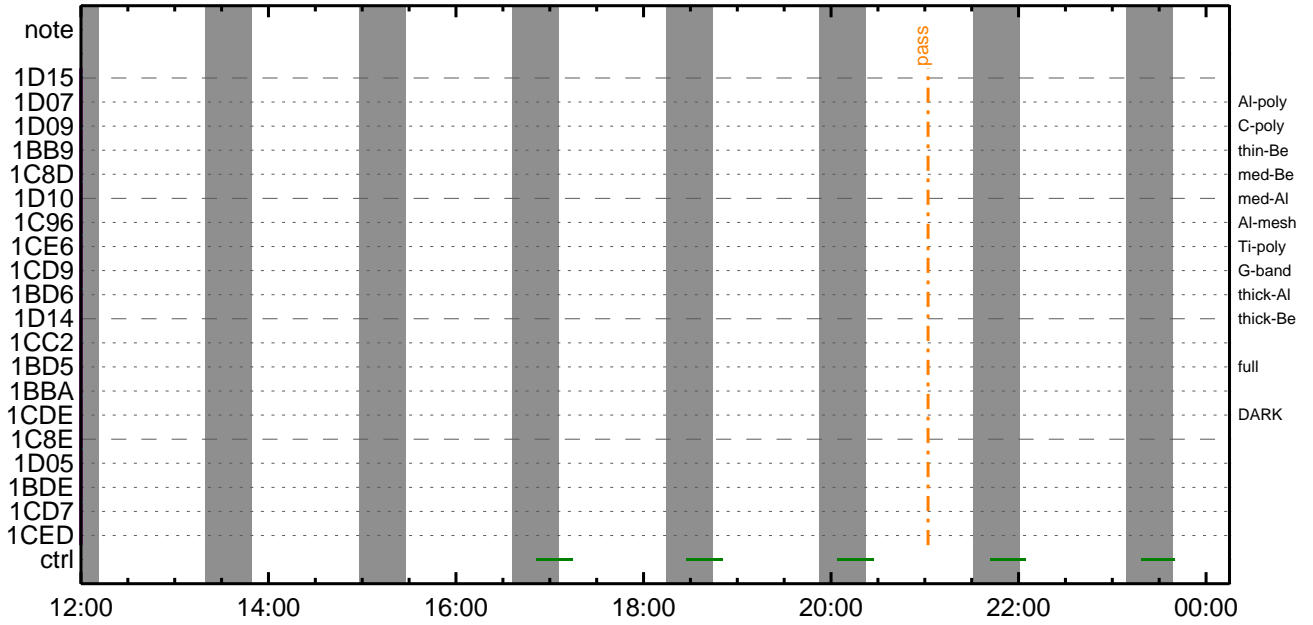
CMDI #0843 2024/05/25



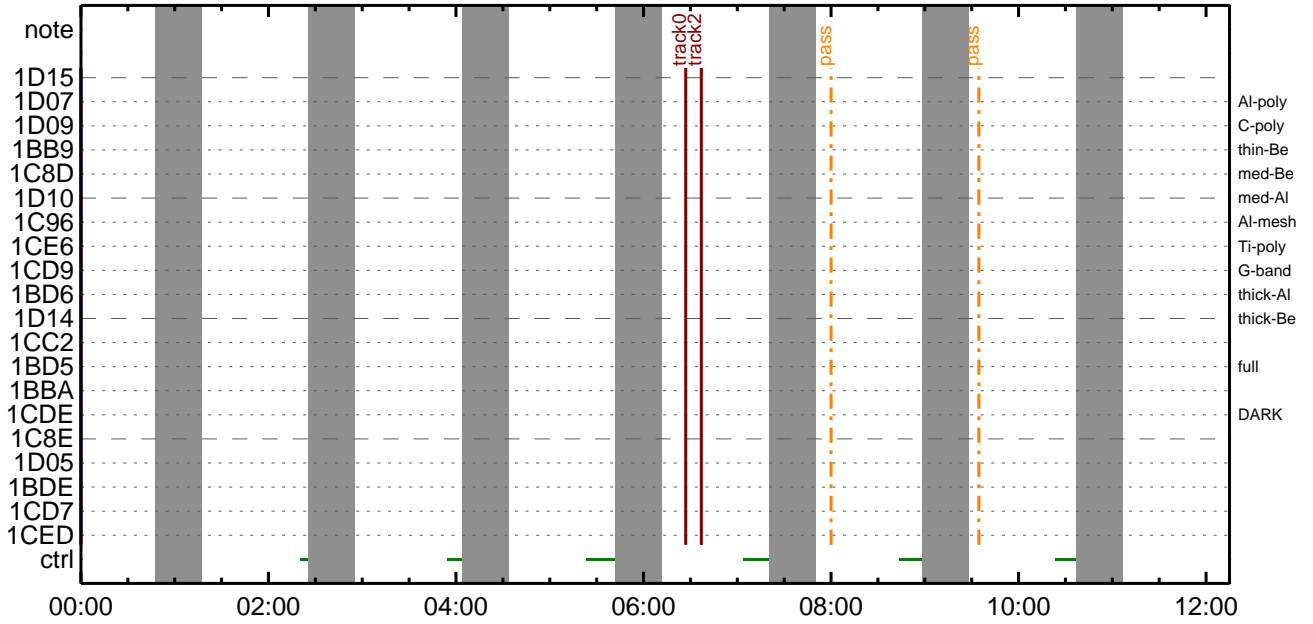
CMDI #0843 2024/05/26



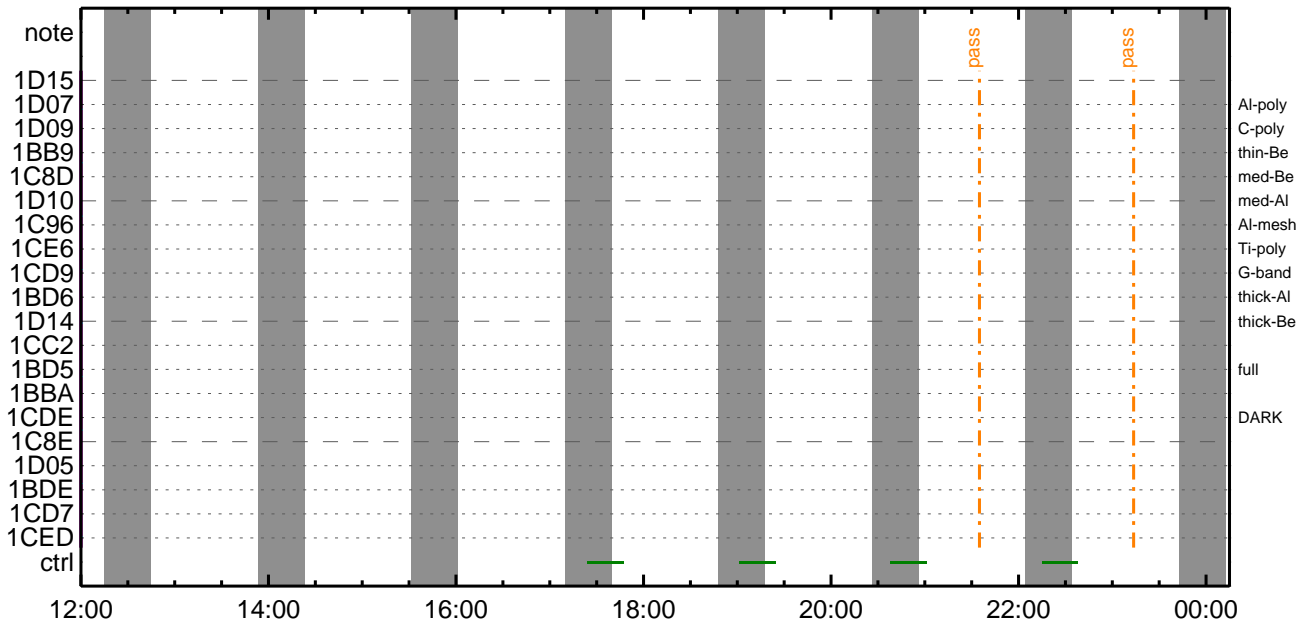
CMDI #0843 2024/05/26



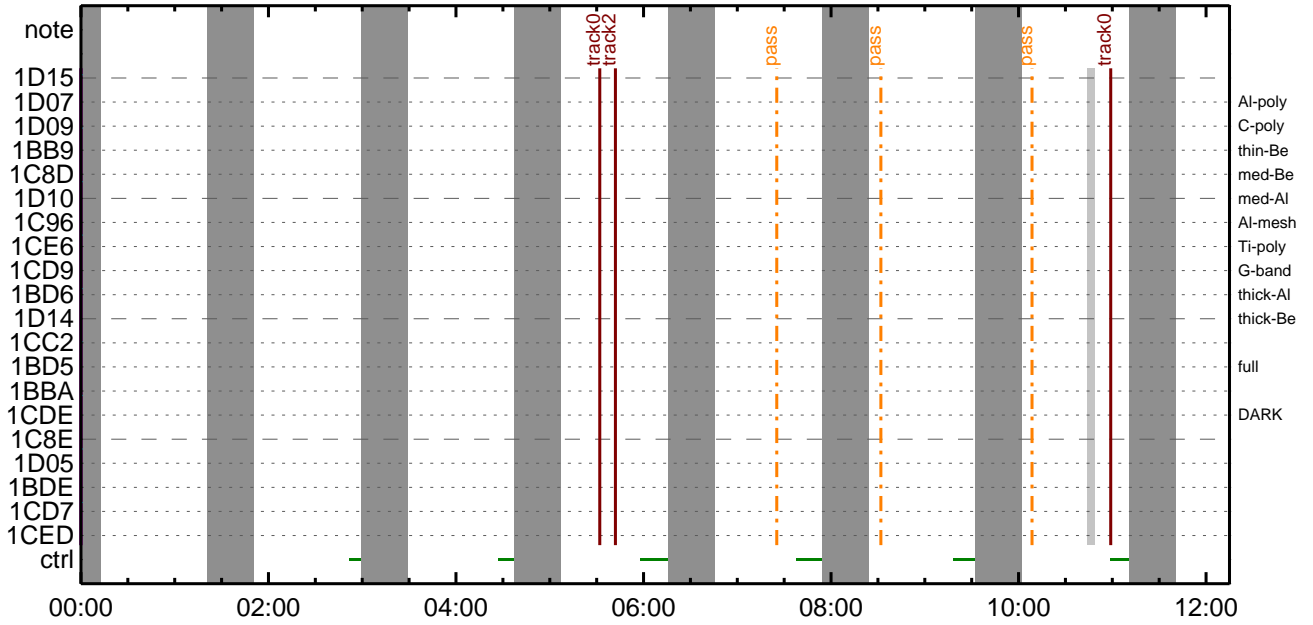
CMDI #0843 2024/05/27



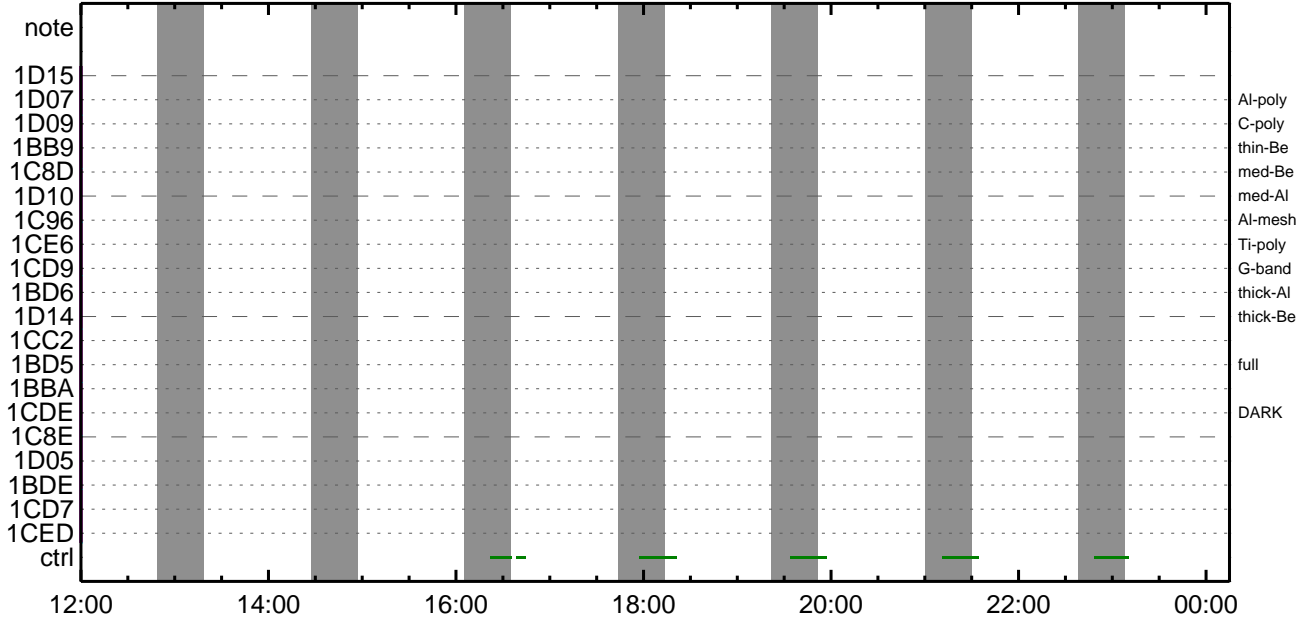
CMDI #0843 2024/05/27



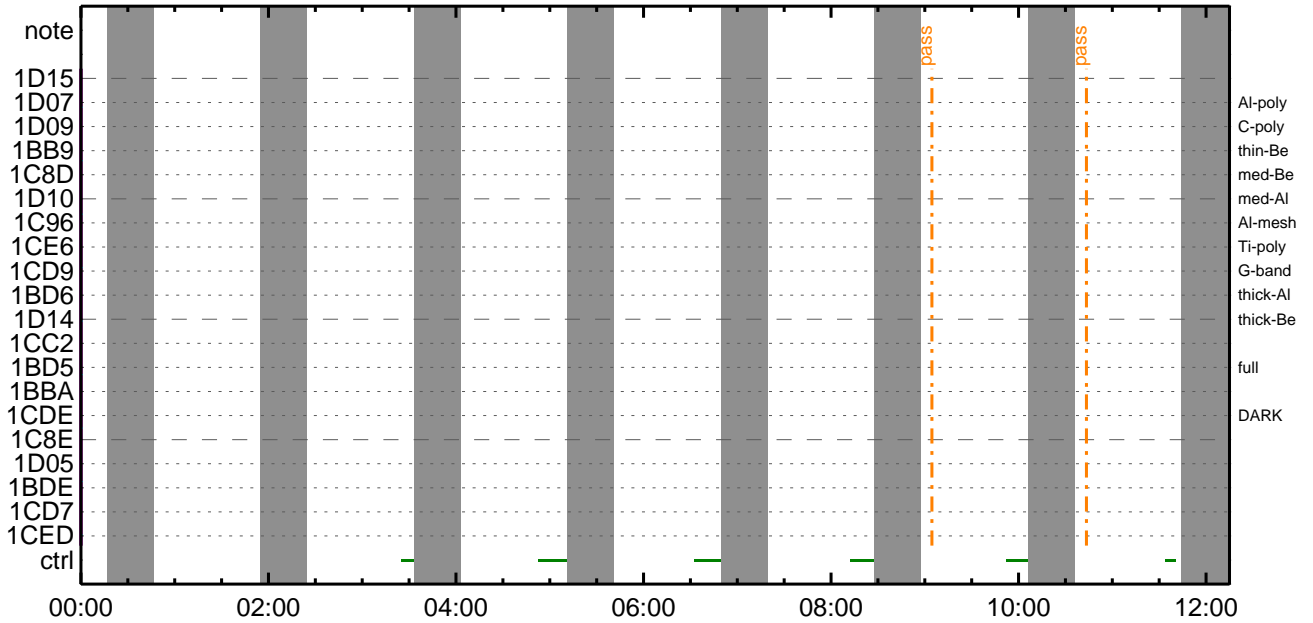
CMDI #0843 2024/05/28



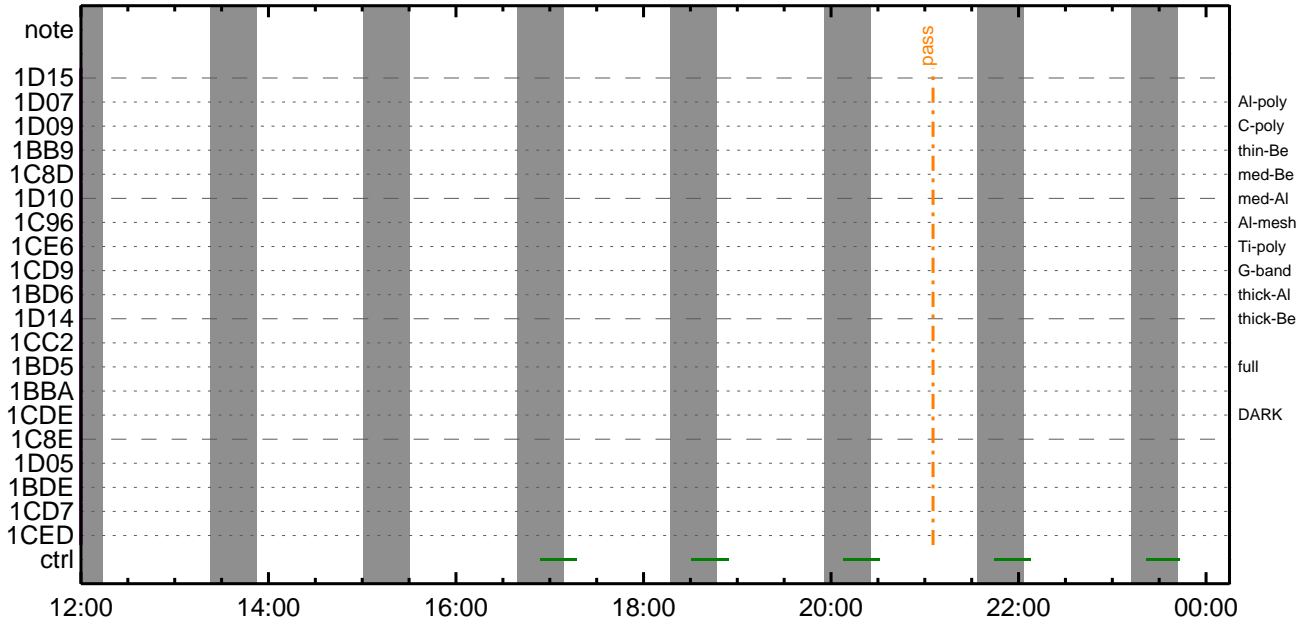
CMDI #0843 2024/05/28



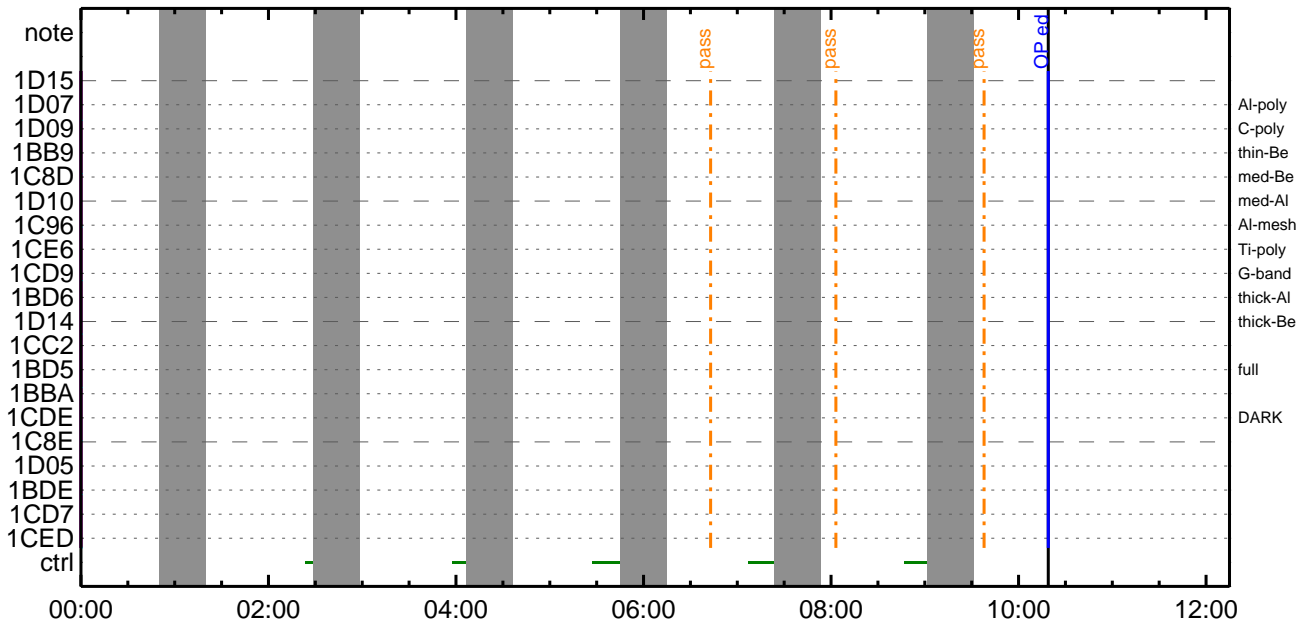
CMDI #0843 2024/05/29



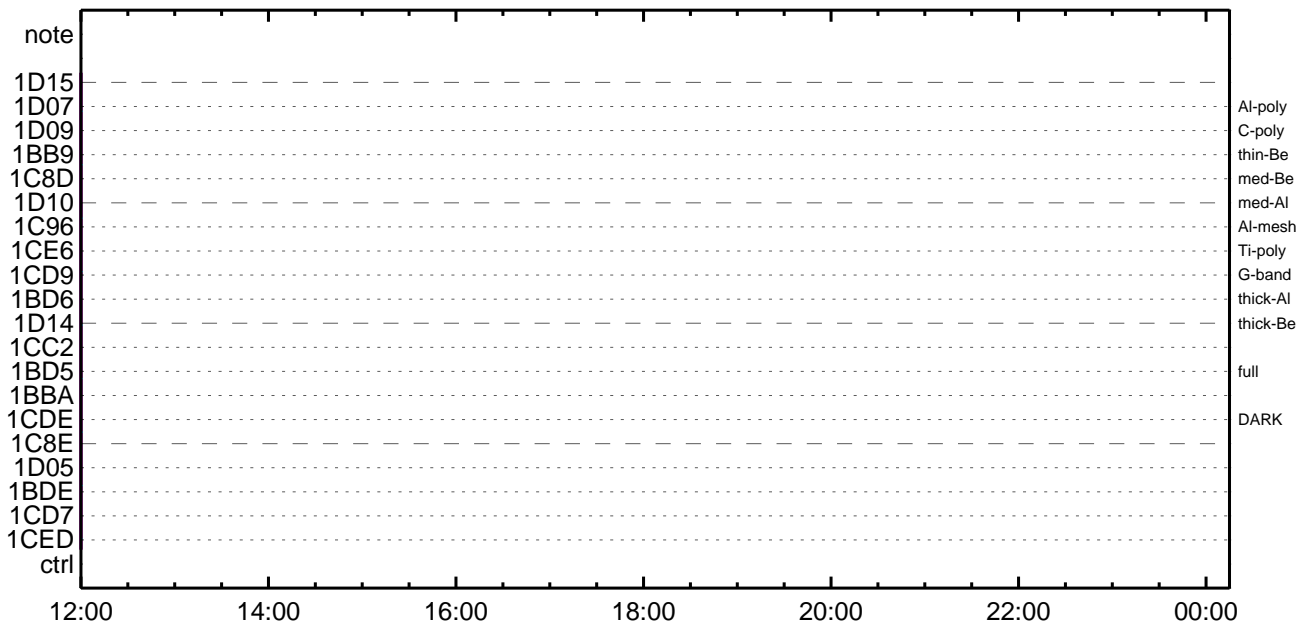
CMDI #0843 2024/05/29



CMDI #0843 2024/05/30



CMDI #0843 2024/05/30




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0096 C.          SET EDUMP I±°iYÑY¹C¹Ôm|³mÈ;£
0097 C.
0098 . C. TIY³YD¥óYÉmòÁDİ¿ (UT)
0099 +. TI 2024-05-25 10:52:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          ¢¢[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2024-05-25 10:52:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          ¢¢[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2024-05-25 10:52:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          ¢¢[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2024-05-25 10:56:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          ¢¢[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C. °Ê¼mİÄê¾iÍÑmİYÁY$YÁY¹àİÜ
0116 C.          ¢¢[HK1_TI_CMD_ENA/DIS]        EQ          ENA
0117 C.          ¢¢[HK1_TI_CMD_NUM]          EQ          4
0118 C.          ¢¢[HK1_NEXT_EXEC_PIM]        EQ          DHU
0119 C.          ¢¢[HK1_NEXT_EXEC_DC]        EQ          0xB3
0120 C.
0121 . C. *****
0122 C. TIİî°èYÁYóY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          ¢¢[HK1_DMP_TOP_ADRS_1]        EQ          07
0129 C.          ¢¢[HK1_DMP_TOP_ADRS_0]        EQ          2B
0130 C.          ¢¢[HK1_DMP_BLOCK_NUM]        EQ          3
0131 C.          ¢¢[HK1_DMP_REPEAT_NUM]       EQ          0
0132 C.          ¢¢[HK1_DMA_DMP_PIM]         EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          ¢¢[HK1_PKT_FORM_NO]          EQ          7
0136 C.          ¢¢[HK1_PKT_GEN_TIME]         EQ          0.25 s
0137 C.          ¢¢[HK1_S_TLM_BIT_RATE]       EQ          32k
0138 C.          ¢¢[HK1_X_TLM_BIT_RATE]      EQ          4M
0139 C.          ¢¢[HK1_DMP_CHK_FLG]         EQ          EXEC
0140 C.
0141 . C. YÁYóY×½ªİ»mò³İÇ$
0142 C.          ¢¢[HK1_DMP_CHK_FLG]         EQ          NON
0143 C.
0144 . C. RAM ID=TI_TBLmİ¾È¹Ç•è²İOKmò³İÇ$
0145 C.
0146 . C. DHU¥â;¼YÉ;Ê¼Y½,¥i;¼YÈ;Ëmòİám¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          ¢¢[HK1_PKT_FORM_NO]          EQ          2
0150 C.          ¢¢[HK1_PKT_GEN_TIME]         EQ          0.5S
0151 C.          ¢¢[HK1_S_TLM_BIT_RATE]       EQ          32K
0152 C.          ¢¢[HK1_X_TLM_BIT_RATE]      EQ          4M
0153 C.
0154 . C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2024-05-25 10:56:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC          (21 02)
0163 +. TI 2024-05-25 10:56:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC          (22)
0166 . C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C. *****
0171 C. SOT TI command set
0172 C. *****
0173 C. Execute, after the success of OP upload.
0174 +. TI 2024-05-25 10:56:16.0
0175 DC 07-F0 MDP_SOT_MODE_STBY
0176 BC          (41)
0177 . C. -----
0178 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0179 C. -----
0180 C. ***** SOT END *****
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2024-05-25 10:56:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC          (c3)
0187 . C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP 'úÃİmİ»ò¾YmÈÄDm¹mèDCBC•×²è *****
0192 C. (¾å°iYóYÁYÉYD¥P¥ÉYÁY¢YÉmÈ¾¼m¼Á»Üm¹mè)
0193 . S. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥Ð¥¹•Ï Daily±;ÍÑ«Ë'Ø«¹«èDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-585 2024-05-25 13:54:59 85 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸSŸÄŸ~¼Ä»Û;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸDŸóŸÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èñ¿ñÄñ•µ°È»Í×ÁÇñÍŸçŸÄŸ×Ÿí;¼ŸÉ;ÈÈèµ•ííÈ;ÈñÈ¼°ÇÖñ•ñ¿¼í¹ÇñÍ;çÄ®, ùñ¹ñèñDñÇÄ÷¿®ñ•ñÈñññ³ñÈ;ñ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_CHG_ENA
0047 BC (20)
0048 . C. Verify EIS_MODE_CHG_FLG is ENA
0049 +. DC 07-FC EIS_MODE_MANU
0050 BC (21 02)
0051 . C. Verify EIS in MANUAL mode
0052 . C. Estimated OBSTBL upload time is 55s
0053 C. *****
0054 C. EIS START OBSTBL LOAD
0055 C. *****
0056 . S. RAM ram-820:EIS_OBSTBL
0057 ( )
0058 +. DC 07-FC EIS_DUMP_OBSTBL
0059 BC (07 07 07 00 00 70 00)
0060 C.
0061 C. Execute, after the success of OBSTBL upload.
0062 C. Set EIS TI-commands
0063 +. TI 2024-05-25 10:56:50.0
0064 DC 07-FC EIS_MODE_CHG_ENA
0065 BC (20)
0066 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0067 C. *****
0068 C. EIS END OBSTBL LOAD
0069 C. *****
0070 C.
0071 . C. ***** MDP ´ûÄÎñÍ»ó¼ŸñÈÄñ¹ñèDCBC•×²è *****
0072 C. (¾å°íŸÓŸÄŸÈŸDŸŸÈŸáŸçŸèñÈ¼¼ñ¼Ä»Ûñ¹ñè)
0073 . S. DC-BC dcbc-402:DCBC
0074 (MDP_known_event)
0075 C.
0076 C.
0077 . C. ***** ŸDŸ¹!•Ï Daily±¿ÍññÈ´Øñ¹ñèDCBC•×²è *****
0078 . S. DC-BC dcbc-153:DCBC
0079 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0080 C.
0081 C.
0082 . C. ;ãLOSŸÁŸSŸÄŸ~¼Ä»Û;ä
0083 C.
0084 . C. ***** LOS *****
0085 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```

main-586 2024-05-25 13:54:59 216 33 SOLAR-B MAIN //
0001  C.
0002  . C. ***** AOS *****
0003  C.
0004  . C. ;ãAOS¥Á¥S¥Á¥-¼Ä»Û;ä
0005  C.
0006  C. ¥À¥ß;¼¥³¥Ð¥ó¥ÉÄ÷ç®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008  C.
0009  . C. ***** AOCs : Reload orbital element (send every contact) *****
0010  C.  Áí;ÉçµÄñ•µ°Ê»Í×ÁÇµÍ¥ç¥Á¥×¥í;¼¥É;ÉÊëµ•ÍÉ;ÉñÈ¼°ÇÖñ•µç¥¼¹çñİ;çÄ®, ùñ¹ñèñÐñÇÁ÷ç®ñ•ñÊññ³ñÈ;£
0011  +. DC 02-8E AOCU_ORB_UPD
0012  C.
0013  C.
0014  . C. *****
0015  C.  XÁ÷ç®µ;ON
0016  C. *****
0017  C.  °EÀ, Í×ÉÝñäLOSñÐñÇñİ»p´Öñò¹ÍÍ,ñ•;çÉÔÍ×ñÈXÁÓNñİ¹ÖñÊñİñÊññ³ñÈ;£
0018  C.
0019  +. DC 03-B4 TCIA_XPA_ON/HI
0020  M. WAIT_SEC 1
0021  + DC 03-84 TCIA_XMOD_ON
0022  M. WAIT_SEC 1
0023  + DC 03-95 TCIA_XMOD_QPSK
0024  C.           çç [HK1_XPA_ON/OFF]           EQ      ON
0025  C.           çç [HK1_XPA_PWR_HI/LO]      EQ      HI
0026  C.           çç [HK1_XMOD_ON/OFF]        EQ      ON
0027  C.           çç [HK1_XMOD_QPSK/PM]       EQ      QPSK
0028  C.
0029  . C.  X¥Ð¥ó¥É¥í¥Á¥-¾ÖÄÖñ¬°ÄÄëñ•µç;µé;ç°É²¼ñÍ°EÀ,¼é½çñò¼Ä¹Öñ¹ñé;£
0030  C.
0031  . C. *****
0032  C.  DR PT1  ÁÌ¼í°EÀ,
0033  C. *****
0034  C.  °° RESTART;ÊPT1;Ëñ•µçµ¾¼¹çñİ;ç°É²¼ñÍ¼Ä¹Öñ»ñ°;çDCBC-150ñØçÊñà;£
0035  C.
0036  . C. ;ãPT1°EÀ,³«»İ;ä
0037  +. DC 01-29 DHU_S/X_VC4_OFF
0038  + DC 06-C8 DR_PT1_REP_SEL
0039  BC          (01 00)
0040  + DC 06-B3 DR_REP_START
0041  + DC 01-32 DHU_X_VC4_ON
0042  C.           çç [HK1_REP_PT_1/2]           EQ      PT1   (¼Ä¹Ö,;¼Ú)
0043  C.           çç [HK1_REP_STA/STP]         EQ      START (¼Ä¹Ö,;¼Ú)
0044  C.           çç [HK1_X_VC4_ON/OFF]        EQ      ON    (¼Ä¹Ö,;¼Ú)
0045  C.
0046  . C. ;ã¥ç¥ó¥É¥ÈÀÙÄØ;ÊÄ°Ä°²óèò;Ë,ãñÍ°EÀ,°E³«;ä
0047  +. DC 06-B3 DR_REP_START
0048  + DC 01-32 DHU_X_VC4_ON
0049  C.           çç [HK1_REP_PT_1/2]           EQ      PT1   (¼Ä¹Ö,;¼Ú)
0050  C.           çç [HK1_REP_STA/STP]         EQ      START (¼Ä¹Ö,;¼Ú)
0051  C.           çç [HK1_X_VC4_ON/OFF]        EQ      ON    (¼Ä¹Ö,;¼Ú)
0052  C.
0053  C.
0054  . C.  PT1°EÀ,ñ¬¼«°E°Ää»ßñ•µç,ã;ç°É²¼ñò¼Ä¹Öñ¹ñé;£
0055  C.  ¥ç¥ó¥É¥ÈÀÙÄØñàÄ°Ä°²óèòñ¬¾áñ¾¼¹çñİ´°İ»ñ¹ñèñÐñÇÄñÀ;£
0056  C.
0057  . C. *****
0058  C.  DR PT2  ÁÌ¼í°EÀ,
0059  C. *****
0060  C.  °° RESTART;ÊPT2;Ëñ•µçµ¾¼¹çñİ;ç°É²¼ñÍ¼Ä¹Öñ»ñ°;çDCBC-151ñØçÊñà;£
0061  C.
0062  . C. ;ãPT2°EÀ,³«»İ;ä
0063  +. DC 01-29 DHU_S/X_VC4_OFF
0064  + DC 06-C8 DR_PT2_REP_SEL
0065  BC          (02 00)
0066  + DC 06-B3 DR_REP_START
0067  + DC 01-32 DHU_X_VC4_ON
0068  C.           çç [HK1_REP_PT_1/2]           EQ      PT2   (¼Ä¹Ö,;¼Ú)
0069  C.           çç [HK1_REP_STA/STP]         EQ      START (¼Ä¹Ö,;¼Ú)
0070  C.           çç [HK1_X_VC4_ON/OFF]        EQ      ON    (¼Ä¹Ö,;¼Ú)
0071  C.
0072  . C. ;ã¥ç¥ó¥É¥ÈÀÙÄØ;ÊÄ°Ä°²óèò;Ë,ãñÍ°EÀ,°E³«;ä
0073  +. DC 06-B3 DR_REP_START
0074  + DC 01-32 DHU_X_VC4_ON
0075  C.           çç [HK1_REP_PT_1/2]           EQ      PT2   (¼Ä¹Ö,;¼Ú)
0076  C.           çç [HK1_REP_STA/STP]         EQ      START (¼Ä¹Ö,;¼Ú)
0077  C.           çç [HK1_X_VC4_ON/OFF]        EQ      ON    (¼Ä¹Ö,;¼Ú)
0078  C.
0079  . C. *****
0080  C.  DR°EÀ,Ää»ß;çXÁ÷ç®µ;OFF
0081  C. *****
0082  C.
0083  . C. ;ãDR°EÀ,Ää»ß;ä
0084  +. DC 06-B4 DR_REP_STOP
0085  + DC 01-29 DHU_S/X_VC4_OFF
0086  C.           çç [HK1_REP_STA/STP]         EQ      STOP
0087  C.           çç [HK1_S_VC4_ON/OFF]        EQ      OFF
0088  C.           çç [HK1_X_VC4_ON/OFF]        EQ      OFF
0089  C.
0090  . C. ;ãXÁ÷ç®µ;OFF;ä
0091  +. DC 03-85 TCIA_XMOD_OFF
0092  M. WAIT_SEC 1
0093  + DC 03-B5 TCIA_XPA_OFF
0094  C.           çç [HK1_XMOD_ON/OFF]         EQ      OFF
0095  C.           çç [HK1_XPA_ON/OFF]         EQ      OFF

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```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG ____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ()
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 05 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 06 85 83 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 07 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 08 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 09 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0a 80 80 08 08)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 08 08)
0142 + DC 07-F0 MDP_XRT_FLD_ENA
0143 BC (d8)
0144 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0145 BC (c8)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 + DC 07-F0 MDP_XRT_AEC_RESET
0149 BC (d0)
0150 + DC 07-F0 MDP_XRT_FLD_RESET
0151 BC (da)
0152 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0153 BC (c4 07)
0154 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0155 BC (c5 0e)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C.
0159 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0160 C.
0161 +. DC 07-F0 MDP_XRT_MODE_OBSV
0162 BC (c2)
0163 +. TI 2024-05-25 10:56:02.0
0164 DC 07-F0 MDP_XRT_MODE_OBSV
0165 BC (c2)
0166 . C. ----- Success Verify ? OK / NG ____
0167 C.
0168 C. ***** XRT END *****
0169 . C. *****
0170 C. SOT table upload
0171 C. *****
0172 . C. < Stop SP table >
0173 +. DC 07-F0 MDP_SP_CTRL_MANU
0174 BC (61)
0175 C. -----
0176 C. MDP_SP_CTRL_MODE = MANU [ ]
0177 C. -----
0178 C.
0179 . C. <Upload SP Observation Table>
0180 . S. RAM ram-286:MDP_OBS_S
0181 ()
0182 C.
0183 . C. < Dump RAMID=MDP_OBS_S >
0184 +. DC 07-F0 MDP_DUMP_SPTBL
0185 BC (83 07 00 00 00 38 b8)
0186 C. -----
0187 C. MDP_OBS_S verify = OK/NG [ ]
0188 C. -----
0189 C.
0190 C. *****
0191 C. SOT TI command set
0192 C. *****
0193 C. Execute, after the success of TBL upload.

```

```
0194 +. TI 2024-05-25 10:56:18.0
0195 DC 07-F0 MDP_SOT_MODE_OBSV
0196 BC (40)
0197 . C. -----
0198 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0199 C. -----
0200 C.
0201 C.
0202 . C. ***** MDP 'úÃîñî»ô¼ýñëâðñ¹ñèDCBC•x²è *****
0203 C. (¾â°îÿÓÿÃÿËÿÐÿËÿâÿçÿèñË¼ññ¼Ã»Ûñ¹ñè)
0204 . S. DC-BC dcbc-402:DCBC
0205 (MDP_known_event)
0206 C.
0207 C.
0208 . C. ***** ÿÐÿ¹•ï Daily±çíññë´øñ¹ñèDCBC•x²è *****
0209 . S. DC-BC dcbc-153:DCBC
0210 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0211 C.
0212 C.
0213 . C. ;ãLOSÿÁÿ$ÿÃÿÿ¼Ã»Û;ä
0214 C.
0215 . C. ***** LOS *****
0216 C.
```

*** OP Sequence for XRT ***

2024/05/25	11:06:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/25	11:06:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/25	11:06:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2024/05/25	11:07:00.0	AOCS_ORe-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	02 06 24 01 b2			
2024/05/25	11:07:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2024/05/25	11:07:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2024/05/25	11:07:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2024/05/25	11:07:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2024/05/25	11:07:26.0	XRT_FLD_RESET_434_OG [0x1b2]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/05/25	11:09:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09			
2024/05/25	11:09:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f			
2024/05/25	11:37:00.0	XRT_Custom_430_OG [0x1ae]						
2024/05/25	11:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/05/25	12:45:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/25	12:45:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/25	12:45:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/05/25	12:45:36.0	XRT_PREFLR_STRT_403_OG [0x193]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/05/25	12:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/05/25	13:15:00.0	XRT_Custom_430_OG [0x1ae]						
2024/05/25	13:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/05/25	14:24:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/25	14:24:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/25	14:24:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/05/25	14:24:06.0	XRT_PREFLR_STRT_403_OG [0x193]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/05/25	14:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/05/25	14:53:30.0	XRT_Custom_430_OG [0x1ae]						
2024/05/25	14:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/05/25	16:02:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/25	16:02:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/25	16:02:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/05/25	16:02:06.0	XRT_PREFLR_STRT_403_OG [0x193]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/05/25	16:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/05/25	16:41:00.0	XRT_Custom_430_OG [0x1ae]						
2024/05/25	16:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/05/25	17:40:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/25	17:40:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/25	17:40:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/05/25	17:40:36.0	XRT_PREFLR_STRT_403_OG [0x193]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/05/25	17:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/05/25	18:17:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/25	18:17:26.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/25	18:17:28.0	XRT_FOCUS_POSITION_406_OG [0x196]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2024/05/25	18:17:30.0	AOCS_ORe-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2024/05/25	18:17:48.0	XRT_FLD_DIS_409_OG [0x199]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2024/05/25	18:17:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2024/05/25	18:17:52.0	XRT_ARS_DIS_435_OG [0x1b3]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2024/05/25	18:20:28.0	XRT_QT_PROG_SET_401_OG [0x191]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
2024/05/25	18:20:30.0	XRT_CTRL_AUTO_408_OG [0x198]						

2024/05/25	18:27:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/25	18:27:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/25	18:27:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2024/05/25	18:27:30.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 06 24 01 b2
2024/05/25	18:27:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2024/05/25	18:27:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2024/05/25	18:27:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2024/05/25	18:27:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2024/05/25	18:27:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/25	18:30:26.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2024/05/25	18:30:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/05/25	18:30:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/25	19:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/25	19:18:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/25	19:18:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/25	19:18:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/25	19:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/25	19:54:30.0	XRT_Custom_430_OG [0x1ae]				
2024/05/25	19:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/25	20:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/25	20:57:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/25	20:57:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/25	20:57:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/25	21:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/25	21:31:31.0	XRT_Custom_430_OG [0x1ae]				
2024/05/25	21:32:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/25	22:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/25	22:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/25	22:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/25	22:35:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/25	22:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/25	23:07:30.0	XRT_Custom_430_OG [0x1ae]				
2024/05/25	23:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/26	00:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/26	00:13:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/26	00:13:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/26	00:13:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/26	00:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/26	00:43:00.0	XRT_Custom_430_OG [0x1ae]				
2024/05/26	00:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/26	01:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/26	01:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/26	01:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/26	01:51:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/26	01:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/26	02:00:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/26	02:00:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2024/05/26	02:01:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00

2024/05/26	02:01:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/05/26	02:01:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/05/26	02:01:20.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/05/26	02:01:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/26	02:01:24.0	XRT_FLD_RESET_438_OG [0x1b6]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/26	02:03:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2024/05/26	02:03:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/05/26	02:21:00.0	XRT_Custom_430_OG [0x1ae]							
2024/05/26	02:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/26	03:21:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/26	03:21:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/26	03:21:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/26	03:21:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/26	03:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/26	03:59:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/26	04:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/26	04:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/26	04:49:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/26	04:49:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/26	04:49:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/26	04:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/26	05:37:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/26	05:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/26	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/26	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/26	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/05/26	06:00:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/05/26	06:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/05/26	06:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/26	06:02:58.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2024/05/26	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/26	06:10:30.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/26	06:10:45.0	XRT_TCIB_XRT_S_HTR_A_ENA_425_OG [0x1a9]							
		TCIB_XRT_S_HTR_A_ENA	0	04-BC					
2024/05/26	06:11:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 06 24 01 b2				
2024/05/27	06:27:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2024/05/27	06:37:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 06 24 01 b2				
2024/05/28	05:32:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2024/05/28	05:42:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 06 24 01 b2				
2024/05/28	10:59:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				