

XRT Timeline to be uploaded on 2012/10/30

Period: 2012/10/30 10:28:00 - 2012/11/03 09:41:00

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Normal mode

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XOB #1941: HOP221 FOXSI - Full Disk Q95 4x4 - C/Poly + Al/Poly+Ti-poly - 1443ms												
Term		Pointing (x, y)				Comment						
10/30 10:40:00 - 10/30 10:55:54		Fixed (936.0, 0.0)				# OP start + 10min - HOP 130 (1/15)						
PROG= 02 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 40 1-time(s) 10.0sec												
└─ C-poly/Open thin-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 22 1-time(s) 10.0sec												
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 11 1-time(s) 10.0sec												
└─ C-poly/Open thin-Be/Open close Safe Dark 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1940: HOP221 FOXSI - multi-filter FW1 / FW2 - 4x4												
Term		Pointing (x, y)				Comment						
10/30 10:58:00 - 10/30 11:11:54		Fixed (468.0, 0.0)				HOP 130 (2/15)						
PROG= 18 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 20 2-time(s) 40.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Norm 32.0s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 29 2-time(s) 25.0sec												
└─ Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 41 1-time(s) 60.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Dark 16.0s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Dark 32.0s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 13 1-time(s) 15.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 44ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 707ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Seqn= 23 1-time(s) 15.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 12ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 250ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Subr= 2 2-time(s) 2.0sec												
└─ Seqn= 5 1-time(s) 15.0sec												
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 63ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec												
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 707ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Seqn= 39 1-time(s) 15.0sec												
└─ C-poly/Ti-poly C-poly/thick-Al close Safe Norm 63ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec												
└─ C-poly/Ti-poly C-poly/thick-Al close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Seqn= 96 1-time(s) 15.0sec												
└─ C-poly/Open C-poly/Open close Safe Norm 24ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec												
└─ C-poly/Open C-poly/Open close Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Seqn= 30 1-time(s) 10.0sec												
└─ C-poly/Open C-poly/Open close Safe Dark 12ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ C-poly/Open C-poly/Open close Safe Dark 250ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 97 1-time(s) 20.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Seqn= 43 1-time(s) 30.0sec												
└─ med-Be/Open med-Be/Open close Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec												
└─ med-Be/Open med-Be/Open close Safe Norm 2.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Seqn= 44 1-time(s) 30.0sec												
└─ med-Al/Open med-Al/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec												
└─ med-Al/Open med-Al/Open close Safe Norm 8.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Seqn= 28 1-time(s) 10.0sec												
└─ Al-poly/Open thin-Be/Open close Safe Norm 16ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec												
└─ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #193F: HOP221 FOXSI - multi-filter FW1 / FW2 - 2x2												
Term		Pointing (x, y)				Comment						
10/30 11:14:00 - 11/01 09:40:54		Fixed (0.0, 0.0)				HOP 130 (3/15)						
PROG= 12 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 31 2-time(s) 30.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 42 2-time(s) 30.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 12 1-time(s) 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Dark 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 27 1-time(s) 30.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												

Seqn= 25		1-time(s)		30.0sec																			
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
Subr= 2		1-time(s)		2.0sec																			
Seqn= 24		1-time(s)		30.0sec																			
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
Seqn= 99		1-time(s)		30.0sec																			
	C-poly/Ti-poly	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
	C-poly/Ti-poly	C-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
Seqn= 26		1-time(s)		30.0sec																			
	C-poly/Open	C-poly/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
	C-poly/Open	C-poly/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
Seqn= 12		1-time(s)		30.0sec																			
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	32.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
Seqn=100		1-time(s)		30.0sec																			
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
Seqn= 14		1-time(s)		30.0sec																			
	med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
	med-Be/Open	med-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
Seqn= 98		1-time(s)		30.0sec																			
	med-Al/Open	med-Al/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
	med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
Seqn= 47		1-time(s)		2.0sec																			
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
Default Filter		Thicker Filter		VLS		mode		image		Exp.		CCD		Bin		ROI: size (center)		Comp.		AEC Buffer		Interval	

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Flare mode

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NOT USED

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Active Region Search

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NOT USED

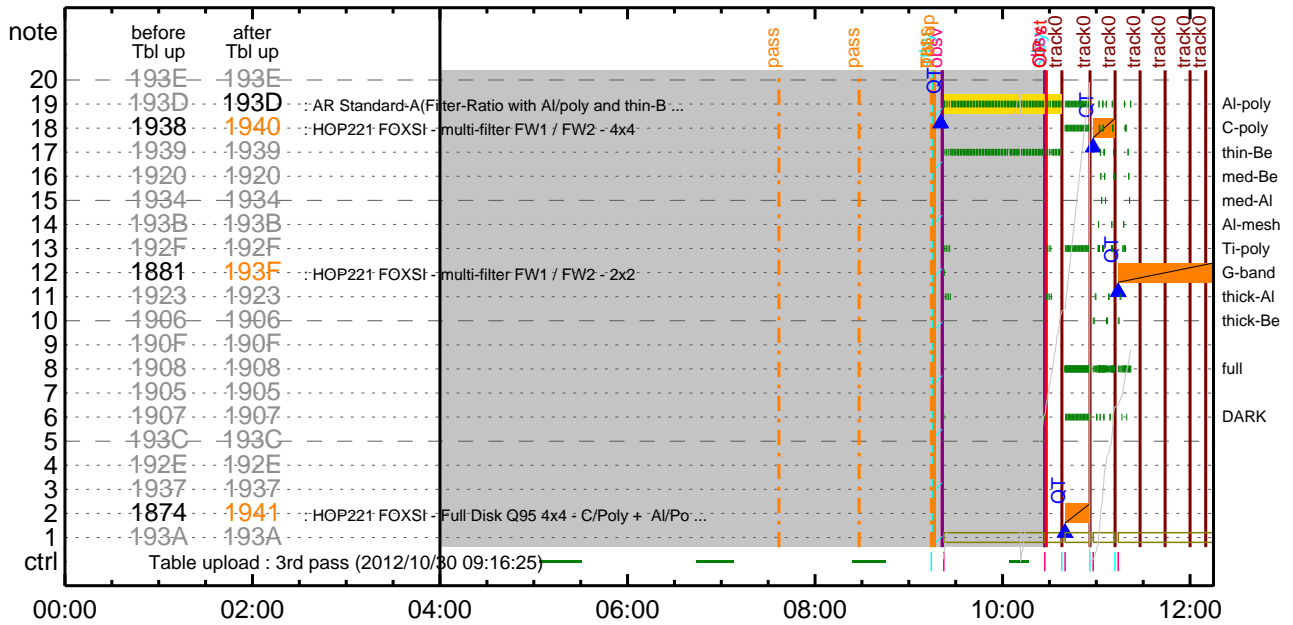
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Flare Detection

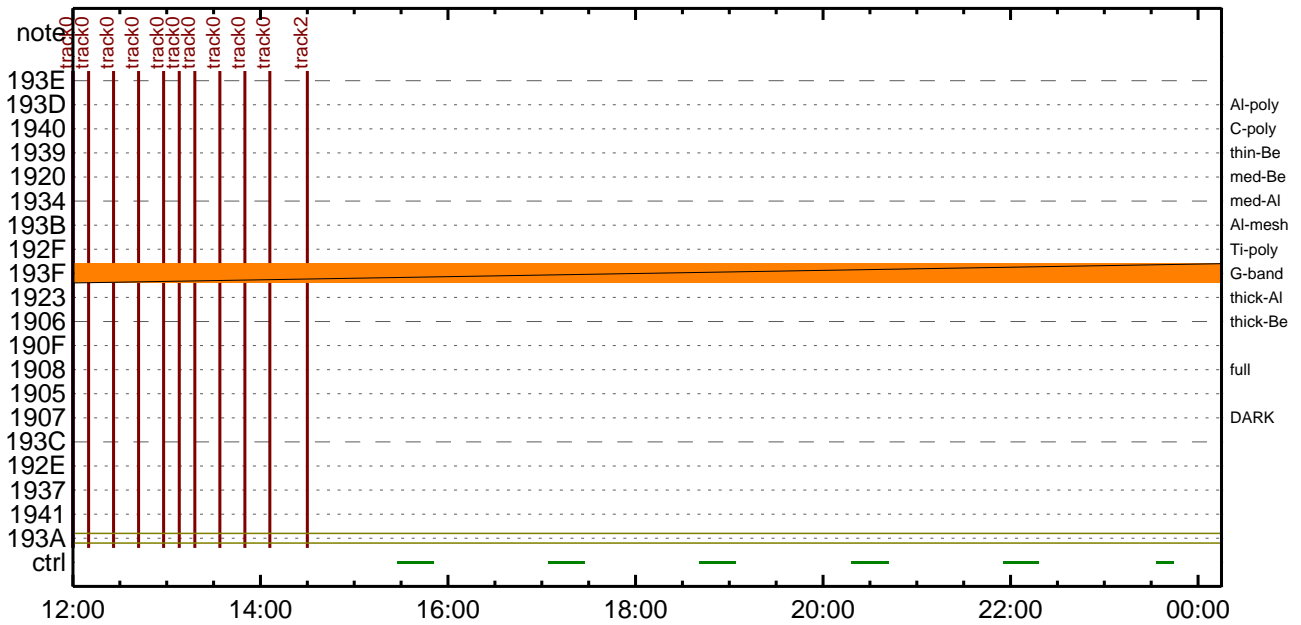
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NOT USED

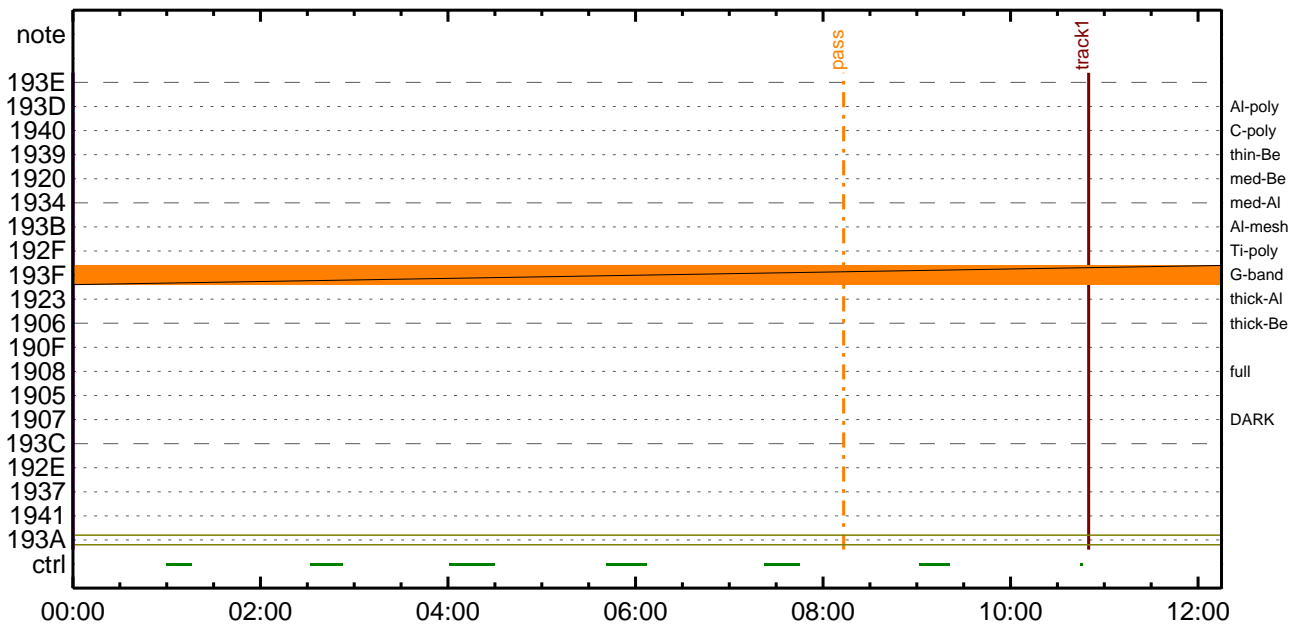
CMDI #0017 2012/10/30



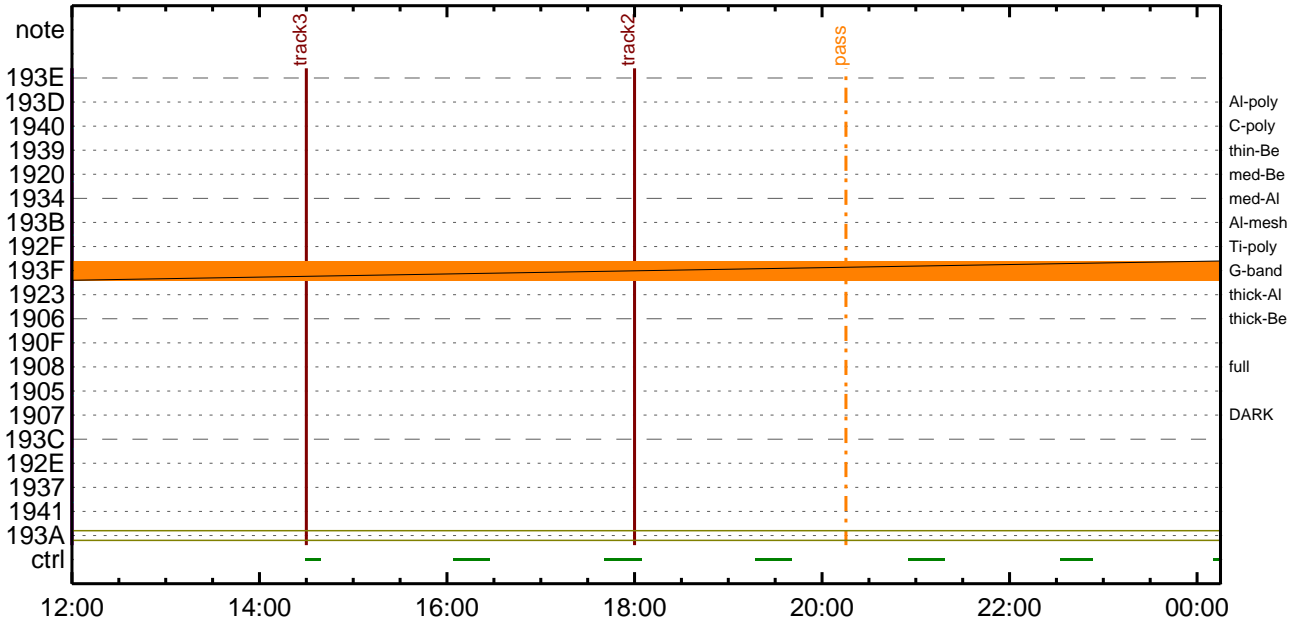
CMDI #0017 2012/10/30



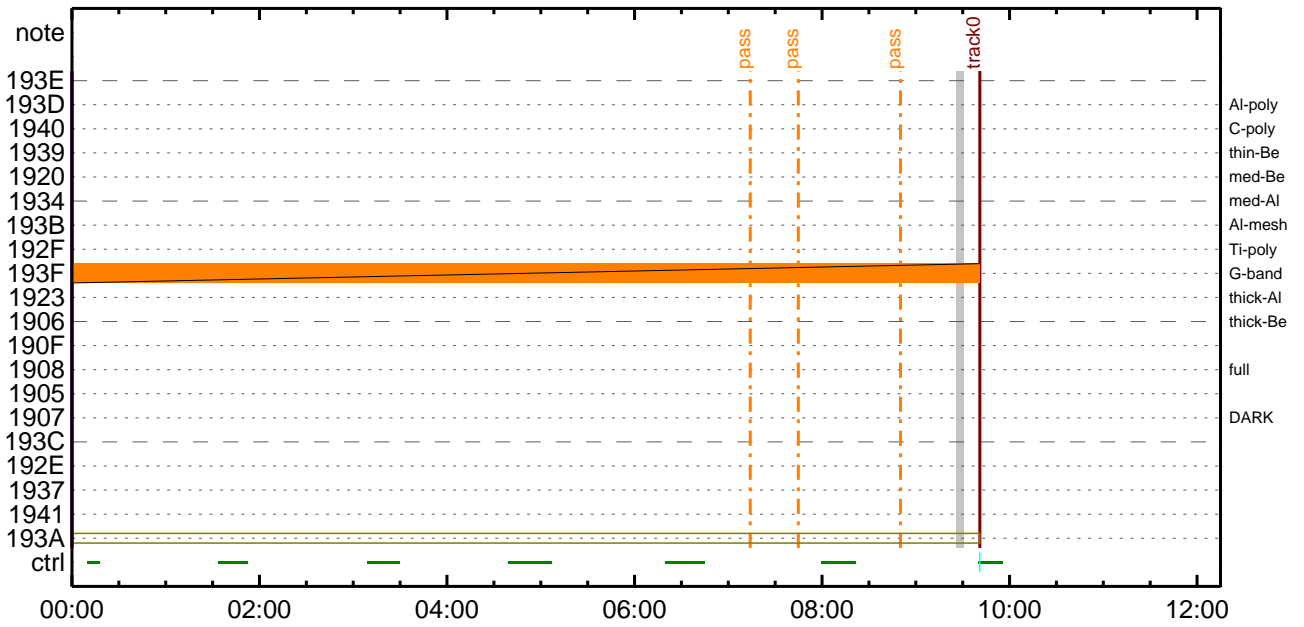
CMDI #0017 2012/10/31



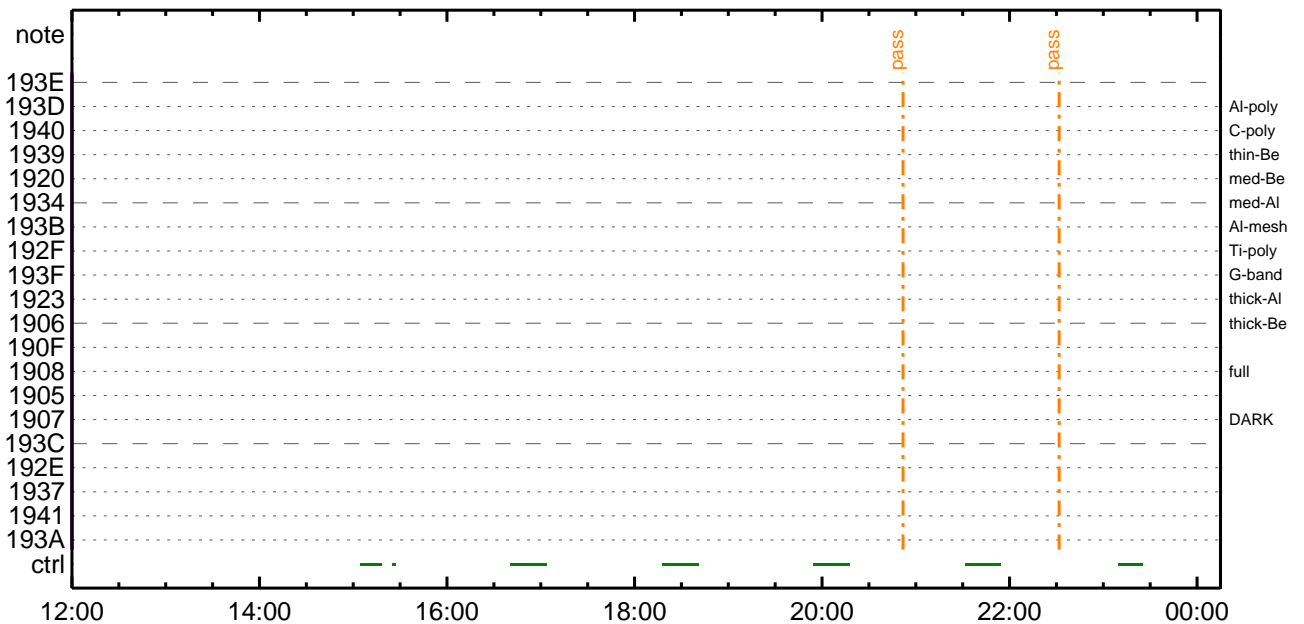
CMDI #0017 2012/10/31



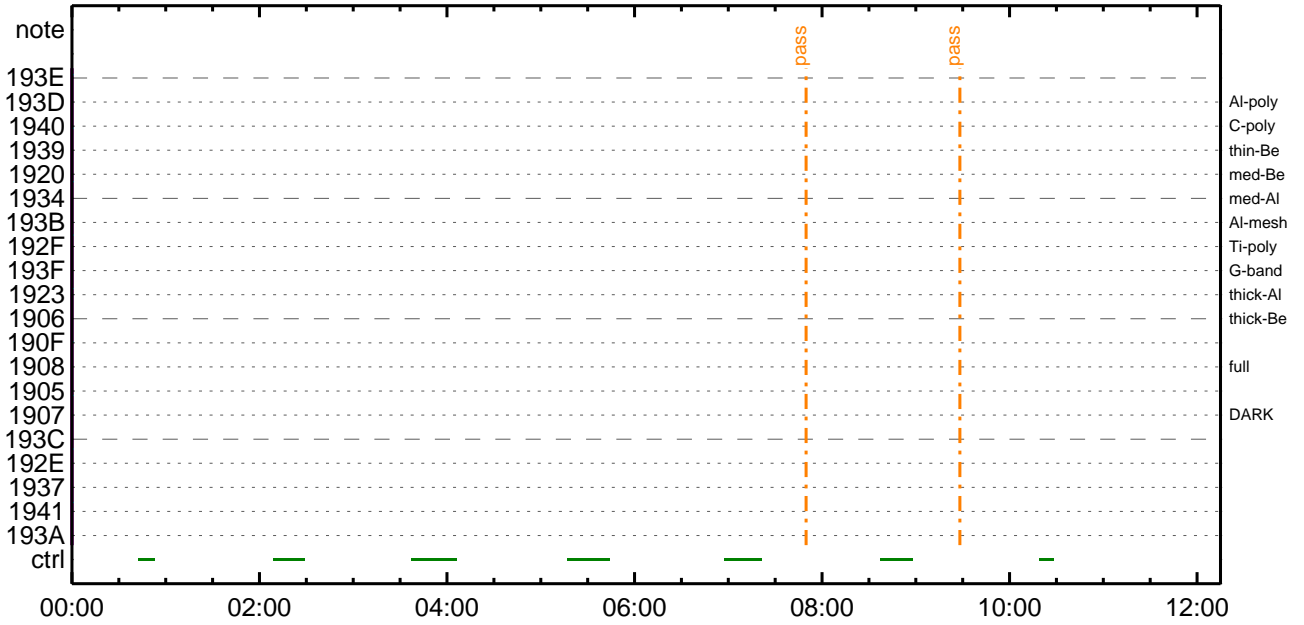
CMDI #0017 2012/11/01



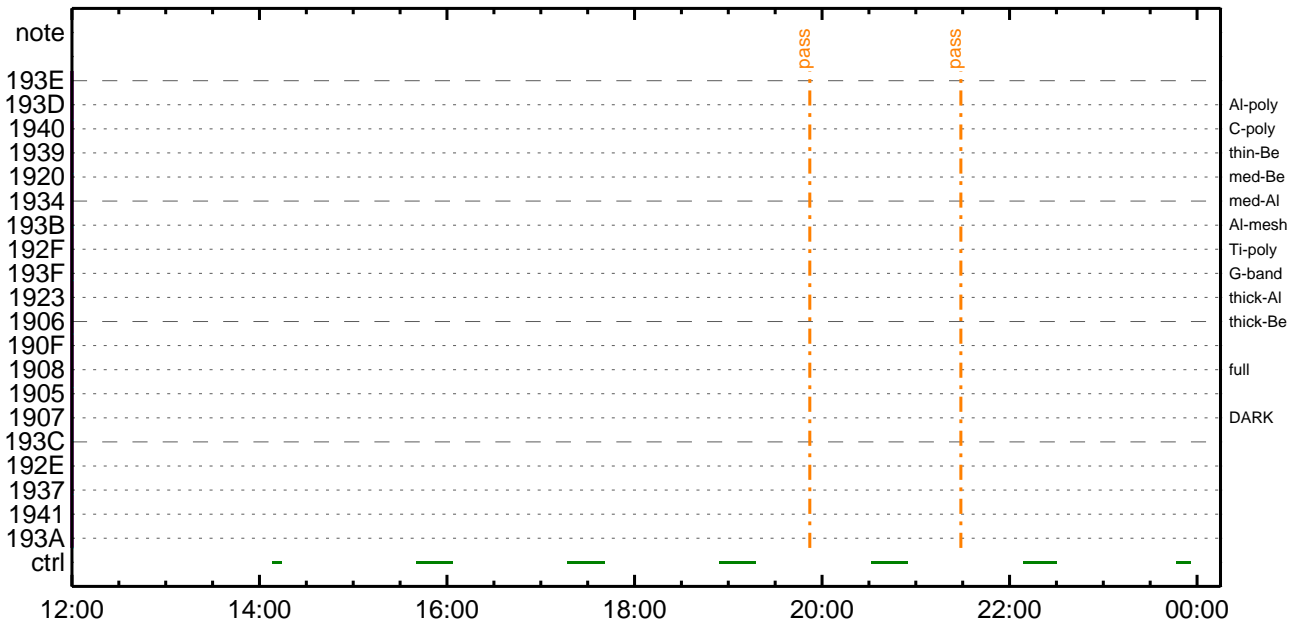
CMDI #0017 2012/11/01



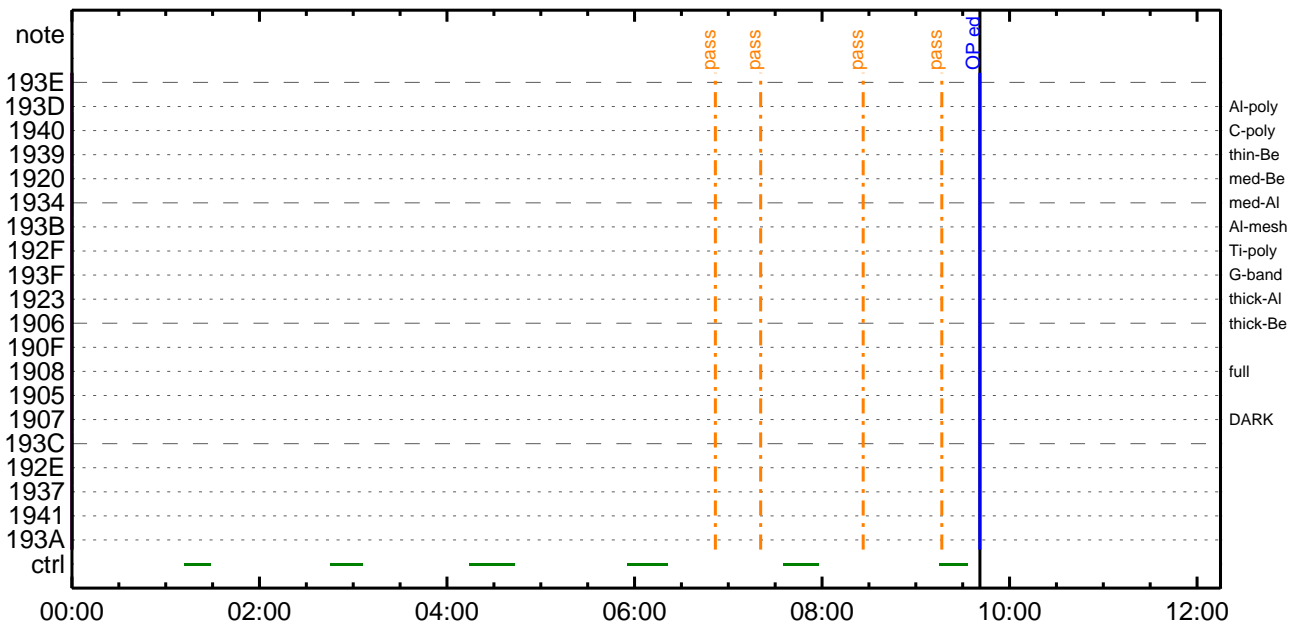
CMDI #0017 2012/11/02



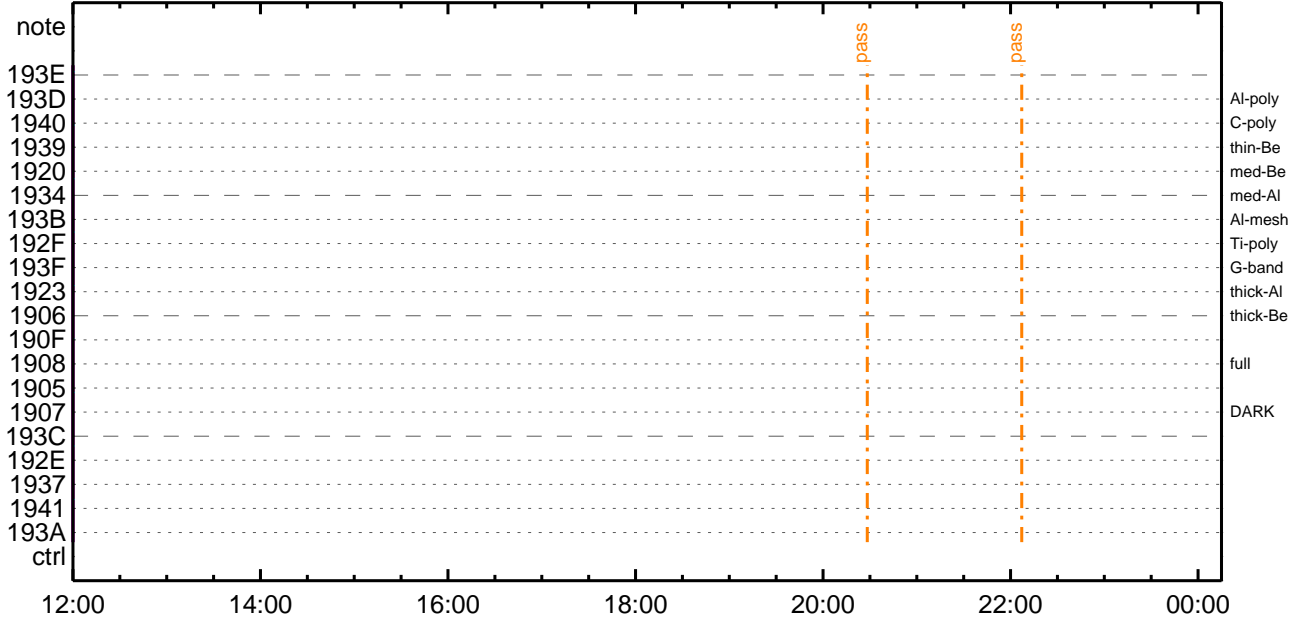
CMDI #0017 2012/11/02



CMDI #0017 2012/11/03



CMDI #0017 2012/11/03



(a) Spacecraft Operation Procedure (real-commands)

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main-237 2012-10-30 14:44:23 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Ä»Û;ã
0005 C.
0006 C. YÀYß;¼Y³YFÝÖYÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ËçµÄµ•µ°Æ»Í×ÁÇçÍYçYÁY×YÍ;¼YÉ;ËÈÈ¼µ•ííÉ;ÈµÈ¼°ÇÖµ•µç¼l¹çµÍ;çÄ®, ùµ¹µèµµçÄ+ç®µ•µèµµµ³µÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+çµ;ON
0016 C. *****
0017 C. ç" °ÆÄ, Í×ÈYµäLOSµµçµÍ»p´Öµð¹íí, µ, ;çÉÖÍ×µÈXÁÖONµí¹Öµèµíµèµµ³µÈ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÖYÉYÍYÁY-¾ÖÄÖµ•µçµé;ç°È²¼µí°ÆÄ, ¼È¼çµð¼Ä¹Öµ¹µé;f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÆÄ,
0033 C. *****
0034 C. ç" RESTART;ËPT1;Ëµ•µçµµ¼l¹çµÍ;ç°È²¼µí°ÆÄ¹Öµµ°;çDCBC-150µØçÈµà;f
0035 C.
0036 . C. ;ãPT1°ÆÄ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÖYÆYÉÄÜÄØ;ÈÄ•Ä°²óÈð;Ë, áµí°ÆÄ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, µ-¼«Æ°Ää»ßµ•µç, á;ç°È²¼µð¼Ä¹Öµ¹µé;f
0055 C. YçYÖYÆYÉÄÜÄØµÄÄ•Ä°²óÈðµ•µçµµ¼l¹çµÍ°í»µ¹µèµµçÄÖµÄ;f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÆÄ,
0059 C. *****
0060 C. ç" RESTART;ËPT2;Ëµ•µçµµ¼l¹çµÍ;ç°È²¼µí°ÆÄ¹Öµµ°;çDCBC-151µØçÈµà;f
0061 C.
0062 . C. ;ãPT2°ÆÄ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÖYÆYÉÄÜÄØ;ÈÄ•Ä°²óÈð;Ë, áµí°ÆÄ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Ää»ß;çXÁ+çµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÄ, Ää»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+çµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 . C. ;aOP/OGY1;4YE;a
0103 . S. OP op-237:OP
0104 ( )
0105 . S. OG og-237:OG
0106 ( )
0107 C.
0108 . C. ;aNMOG&OPf^°eYAYOX;a
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C.          çç[HK1_DMP_TOP_ADRS_1]           EQ      40
0113 C.          çç[HK1_DMP_TOP_ADRS_0]           EQ      0
0114 C.          çç[HK1_DMP_BLOCK_NUM]           EQ     127
0115 C.          çç[HK1_DMP_REPEAT_NUM]          EQ      0
0116 C.          çç[HK1_DMA_DMP_PIM]             EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C.          çç[HK1_PKT_FORM_NO]             EQ      7
0120 C.          çç[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121 C.          çç[HK1_S_TLM_BIT_RATE]          EQ     32k
0122 C.          çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0123 C.          çç[HK1_DMP_CHK_FLG]            EQ     EXEC
0124 . C. YAYOXx½^aÎ»ò³ÎÇ§
0125 C.          çç[HK1_DMP_CHK_FLG]             EQ     NON
0126 . C. RAM ID=NMOGαî¼E¹ç•ë²ÏOKαò³ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C.          çç[HK1_DMP_TOP_ADRS_1]           EQ     41
0132 C.          çç[HK1_DMP_TOP_ADRS_0]           EQ      0
0133 C.          çç[HK1_DMP_BLOCK_NUM]           EQ     127
0134 C.          çç[HK1_DMP_REPEAT_NUM]          EQ      0
0135 C.          çç[HK1_DMA_DMP_PIM]             EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C.          çç[HK1_PKT_FORM_NO]             EQ      7
0139 C.          çç[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140 C.          çç[HK1_S_TLM_BIT_RATE]          EQ     32k
0141 C.          çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0142 C.          çç[HK1_DMP_CHK_FLG]            EQ     EXEC
0143 . C. YAYOXx½^aÎ»ò³ÎÇ§
0144 C.          çç[HK1_DMP_CHK_FLG]             EQ     NON
0145 . C. RAM ID=NMOGαî¼E¹ç•ë²ÏOKαò³ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C.          çç[HK1_DMP_TOP_ADRS_1]           EQ     42
0151 C.          çç[HK1_DMP_TOP_ADRS_0]           EQ      0
0152 C.          çç[HK1_DMP_BLOCK_NUM]           EQ     65
0153 C.          çç[HK1_DMP_REPEAT_NUM]          EQ      0
0154 C.          çç[HK1_DMA_DMP_PIM]             EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C.          çç[HK1_PKT_FORM_NO]             EQ      7
0158 C.          çç[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159 C.          çç[HK1_S_TLM_BIT_RATE]          EQ     32k
0160 C.          çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0161 C.          çç[HK1_DMP_CHK_FLG]            EQ     EXEC
0162 . C. YAYOXx½^aÎ»ò³ÎÇ§
0163 C.          çç[HK1_DMP_CHK_FLG]             EQ     NON
0164 . C. RAM ID=NMOG, RAM ID=OPαî¼E¹ç•ë²ÏOKαò³ÎÇ§
0165 C.
0166 . C. ***** °E²¼αî¼Ä´¶Á°EÉ¬α°Á÷¿@ (¼áµ-YAYOXx½^e¼çαòÄÖæαç¼^a°¬ò¼i¹çαçã) *****
0167 C. DHUYâ;4YE;E¼Y½, Y1;4YE;Eòîãα¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C.          çç[HK1_PKT_FORM_NO]             EQ      2
0171 C.          çç[HK1_PKT_GEN_TIME]            EQ     0.5S
0172 C.          çç[HK1_S_TLM_BIT_RATE]          EQ     32K
0173 C.          çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE |§ OPOG UPLOADα¬Á÷¿@NGUî¼i¹ç;ç°E²¼αî¼TI-CMDÁ÷¿@αî¼Á¹Ôα•αEααα³αE;f
0180 C.          αPα¿;çSETαEDUMPAîÆ±°iYNY¹αÇ¹Ôα|α³αE;f
0181 C.
0182 . C. TIY³YpYóYEòòÄDî¿(UT)
0183 +. TI 2012-10-30 10:23:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.          çç[HK1_TI_CMD_NUM]               EQ     1COUNTUP
0186 C.
0187 +. TI 2012-10-30 10:23:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.          çç[HK1_TI_CMD_NUM]               EQ     1COUNTUP
0190 C.
0191 +. TI 2012-10-30 10:23:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.          çç[HK1_TI_CMD_NUM]               EQ     1COUNTUP

```



```

0194 C.
0195 +. TI 2012-10-30 10:27:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αİÄē%İİñαİŷÄŷ§ŷÄŷ-¹àİÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                    EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                        EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                     EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIİİ°êŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                    EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                    EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                     EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]                    EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]                       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                       EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]                       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªİ»αò³İÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]                       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαİ¼È¹ç•ē²İOKαò³İÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷİ;¼ŷÈ;Èαòİāα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                       EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]                       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                     EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2012-10-30 10:27:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC          (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2012-10-30 10:27:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC          (21 02)
0258 +. TI 2012-10-30 10:27:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC          (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2012-10-30 10:27:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP `ûÃİαİ»ö¼ŷαÈÄα¹αēDCBC•x²è *****
0276 C. (¼á°İŷÖŷÄŷÈŷŷŷÄŷçŷēøÈ¼αα¼Ä»Ûα¹αē)
0277 C. S. DC-BC dcbc-402:DCBC
0278 C. (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•İ Daily±çİñαÈ'Øα¹αēDCBC•x²è *****
0282 C. S. DC-BC dcbc-153:DCBC
0283 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-239 2012-10-30 14:44:23 251 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÅYÅY-¼Å»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFYOYÉA+¿©
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èõ¿ðÅð•μ°È»Í×ÁÇõÍYÇYÅY×YÍ;¼YÉ;ÈÈÈ¼μ°ííÉ;ÈðÈ¼°ÇÖð•σ¿¼í¹çõÍ;çÀ®, ùσ¹ðÈððçÀ+¿©ð•ðÈðð³ðÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿@μ;ON
0016 C. *****
0017 C. ç“ °ÈÀ, í×ÈYðäLOSððçõÍ»p´Öðð¹íí, ð•; çÉÔÍ×ðÈXÁÖONðÍ¹ÔðÈð¹ðÈðð³ðÈ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C.          çç[HK1_XPA_ON/OFF]          EQ        ON
0025 C.          çç[HK1_XPA_PWR_HI/LO]       EQ        HI
0026 C.          çç[HK1_XMOD_ON/OFF]         EQ        ON
0027 C.          çç[HK1_XMOD_QPSK/PM]        EQ        QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÅY-¾ÖÄÖð¬°ÄÄÈð•σ¿ðé; ç°È²¼ðí°ÈÀ, ¼È¼çðð¼Ä¹Ôð¹ðé; f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÈÀ,
0033 C. *****
0034 C. ç“ RESTART; ÈPT1; Èð•σ¿ð¼¼í¹çõÍ; ç°È²¼ðí°ÈÀ¹Ôð»ð°; çDCBC-150ðØ¿Èðà; f
0035 C.
0036 . C. ;ãPT1°ÈÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC          (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C.          çç[HK1_REP_PT_1/2]          EQ        PT1      (¼Ä¹Ô, ;¼Ú)
0043 C.          çç[HK1_REP_STA/STP]         EQ        START   (¼Ä¹Ô, ;¼Ú)
0044 C.          çç[HK1_X_VC4_ON/OFF]        EQ        ON       (¼Ä¹Ô, ;¼Ú)
0045 C.
0046 . C. ;ãYçYOYÅYÈÄÜÁØ; ÈÄ•Ä°²óÈð; È, àσí°ÈÀ, °È³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C.          çç[HK1_REP_PT_1/2]          EQ        PT1      (¼Ä¹Ô, ;¼Ú)
0050 C.          çç[HK1_REP_STA/STP]         EQ        START   (¼Ä¹Ô, ;¼Ú)
0051 C.          çç[HK1_X_VC4_ON/OFF]        EQ        ON       (¼Ä¹Ô, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÈÀ, ð¬¼«È°Ää»ßð•σ¿, à; ç°È²¼ð¼¼Ä¹Ôð¹ðé; f
0055 C. YçYOYÅYÈÄÜÁØððÄÄ•Ä°²óÈðð¬¾áð¼¼í¹çõÍ´°í»ð¹ðÈððçÀÖðÄ; f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÈÀ,
0059 C. *****
0060 C. ç“ RESTART; ÈPT2; Èð•σ¿ð¼¼í¹çõÍ; ç°È²¼ðí°ÈÀ¹Ôð»ð°; çDCBC-151ðØ¿Èðà; f
0061 C.
0062 . C. ;ãPT2°ÈÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC          (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C.          çç[HK1_REP_PT_1/2]          EQ        PT2      (¼Ä¹Ô, ;¼Ú)
0069 C.          çç[HK1_REP_STA/STP]         EQ        START   (¼Ä¹Ô, ;¼Ú)
0070 C.          çç[HK1_X_VC4_ON/OFF]        EQ        ON       (¼Ä¹Ô, ;¼Ú)
0071 C.
0072 . C. ;ãYçYOYÅYÈÄÜÁØ; ÈÄ•Ä°²óÈð; È, àσí°ÈÀ, °È³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C.          çç[HK1_REP_PT_1/2]          EQ        PT2      (¼Ä¹Ô, ;¼Ú)
0076 C.          çç[HK1_REP_STA/STP]         EQ        START   (¼Ä¹Ô, ;¼Ú)
0077 C.          çç[HK1_X_VC4_ON/OFF]        EQ        ON       (¼Ä¹Ô, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÈÀ, Ää»ß; çXÁ+¿@μ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÀ, Ää»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C.          çç[HK1_REP_STA/STP]         EQ        STOP
0087 C.          çç[HK1_S_VC4_ON/OFF]        EQ        OFF
0088 C.          çç[HK1_X_VC4_ON/OFF]        EQ        OFF
0089 C.
0090 . C. ;ãXÁ+¿@μ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C.          çç[HK1_XMOD_ON/OFF]         EQ        OFF
0095 C.          çç[HK1_XPA_ON/OFF]          EQ        OFF
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ()
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_FLD_DIS
0133 BC (d9)
0134 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0135 BC (c9)
0136 + DC 07-F0 MDP_XRT_AEC_RESET
0137 BC (d0)
0138 + DC 07-F0 MDP_XRT_ARS_DIS
0139 BC (d5)
0140 + DC 07-F0 MDP_XRT_FLD_RESET
0141 BC (da)
0142 + DC 07-F0 MDP_XRT_QT_PROG_SET
0143 BC (c4 13)
0144 . C. ----- Success Verify ? OK / NG ____
0145 C.
0146 C.
0147 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0148 C.
0149 +. DC 07-F0 MDP_XRT_MODE_OBSV
0150 BC (c2)
0151 +. DC 07-F0 MDP_XRT_CTRL_AUTO
0152 BC (c0)
0153 +. TI 2012-10-30 10:27:02.0
0154 DC 07-F0 MDP_XRT_MODE_OBSV
0155 BC (c2)
0156 +. TI 2012-10-30 10:27:04.0
0157 DC 07-F0 MDP_XRT_CTRL_AUTO
0158 BC (c0)
0159 . C. ----- Success Verify ? OK / NG ____
0160 C.
0161 C. ***** XRT END *****
0162 . C. *****
0163 C. SOT table upload
0164 C. *****
0165 . C. < Stop FG table >
0166 +. DC 07-F0 MDP_FG_CTRL_MANU
0167 BC (51)
0168 . C. -----
0169 C. MDP_FG_CTRL_MODE = MANU [ ]
0170 C. -----
0171 C.
0172 . C. <Upload FG Observation Table>
0173 . S. RAM ram-268:MDP_OBS_F
0174 ()
0175 C.
0176 . C. < Dump RAMID=MDP_OBS_F >
0177 +. DC 07-F0 MDP_DUMP_FGTBL
0178 BC (82 07 00 00 00 38 b8)
0179 C. -----
0180 C. MDP_OBS_F verify = OK/NG [ ]
0181 C. -----
0182 C.
0183 . C. < Stop SP table >
0184 +. DC 07-F0 MDP_SP_CTRL_MANU
0185 BC (61)
0186 C. -----
0187 C. MDP_SP_CTRL_MODE = MANU [ ]
0188 C. -----
0189 C.
0190 . C. <Upload SP Observation Table>
0191 . S. RAM ram-288:MDP_OBS_S
0192 ()
0193 C.

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0194 . C. < Dump RAMID=MDP_OBS_S >
0195 +. DC 07-F0 MDP_DUMP_SPTBL
0196 BC (83 07 00 00 00 38 b8)
0197 C. -----
0198 C. MDP_OBS_S verify = OK/NG [ ]
0199 C. -----
0200 C.
0201 . C. < Upload DPL table >
0202 C.
0203 C. ¥ç¥Ã¥×¥í;¼¥É°îÁ°°ÊSTS_CHK°ðOFF°È°¹°è
0204 C.
0205 . S. RAM ram-271:MDP_DPL
0206 ( )
0207 C.
0208 . C. < Dump RAMID=MDP_DPL >
0209 +. DC 07-F0 MDP_DUMP_FGTBL
0210 BC (82 07 00 38 b8 00 40)
0211 C. -----
0212 C. MDP_DPL verify = OK [ ]
0213 C. -----
0214 C.
0215 C. STS_CHK°ðON°È°¹°è
0216 C.
0217 . C. < Update MDP DSC PAR1 >
0218 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0219 BC (4c)
0220 C. MDP_CMD_CODE = F04C0700[ ]
0221 C. MDP_CMD_CNT (count-up 1) [ ]
0222 C. -----
0223 C.
0224 . C.
0225 C. *****
0226 C. SOT TI command set
0227 C. *****
0228 C. Execute, after the success of TBL upload.
0229 +. TI 2012-10-30 10:27:18.0
0230 DC 07-F0 MDP_SOT_MODE_OBSV
0231 BC (40)
0232 C. -----
0233 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0234 C. -----
0235 C.
0236 C.
0237 . C. ***** MDP `ûÃîî»ò¼¥°È°¹°èDCBC•x²è *****
0238 C. (¼°î¥Ö¥Ã¥È¥Ï¥É¥à¥ç¥è°È¼°¼°¼°»Û°¹°è)
0239 . S. DC-BC dcbc-402:DCBC
0240 (MDP_known_event)
0241 C.
0242 C.
0243 . C. ***** ¥D¥¹•Ï Daily±;îÑ°È`Ø°¹°èDCBC•x²è *****
0244 . S. DC-BC dcbc-153:DCBC
0245 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0246 C.
0247 C.
0248 . C. ;ãLOS¥Ã¥S¥Ã¥`¼°¼°¼°»Û;ã
0249 C.
0250 . C. ***** LOS *****
0251 C.

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XRT_OGLIST_0017.chk

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*** OP Sequence for XRT ***

2012/10/30	10:37:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/10/30	10:37:56.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/10/30	10:38:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	00 00 00 ac cd			
2012/10/30	10:38:16.0	XRT_FLD_DIS_404_OG [0x194]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/10/30	10:38:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/10/30	10:38:20.0	XRT_ARS_DIS_422_OG [0x1a6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/10/30	10:39:58.0	XRT_QT_PROG_SET_414_OG [0x19e]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02			
2012/10/30	10:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/10/30	10:55:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/10/30	10:55:56.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/10/30	10:56:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 d6 67			
2012/10/30	10:56:16.0	XRT_FLD_DIS_404_OG [0x194]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/10/30	10:56:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/10/30	10:56:20.0	XRT_AEC_RESET_423_OG [0x1a7]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/10/30	10:56:22.0	XRT_ARS_DIS_425_OG [0x1a9]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/10/30	10:57:58.0	XRT_QT_PROG_SET_415_OG [0x19f]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12			
2012/10/30	10:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/10/30	11:11:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/10/30	11:11:56.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/10/30	11:12:00.0	AOCS_Ore-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2012/10/30	11:12:16.0	XRT_FLD_DIS_404_OG [0x194]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/10/30	11:12:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/10/30	11:12:20.0	XRT_ARS_DIS_422_OG [0x1a6]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/10/30	11:13:58.0	XRT_QT_PROG_SET_449_OG [0x1c1]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c			
2012/10/30	11:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/10/30	11:28:00.0	AOCS_Ore-point_Start_4_OG [0x09a]						
		AOCU_NM	5	02-76	00 00 00 29 99			
2012/10/30	11:30:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_439_OG [0x1b7]						
		TCIB_XRT_S_HTR_A_ENA	0	04-BC				
2012/10/30	11:44:00.0	AOCS_Ore-point_Start_5_OG [0x09b]						
		AOCU_NM	5	02-76	00 00 00 53 33			
2012/10/30	12:00:00.0	AOCS_Ore-point_Start_6_OG [0x09c]						
		AOCU_NM	5	02-76	00 d6 36 b7 8e			
2012/10/30	12:10:00.0	AOCS_Ore-point_Start_7_OG [0x09d]						
		AOCU_NM	5	02-76	00 b4 b5 db 75			
2012/10/30	12:26:00.0	AOCS_Ore-point_Start_8_OG [0x09e]						
		AOCU_NM	5	02-76	00 ac 5b 00 00			
2012/10/30	12:42:00.0	AOCS_Ore-point_Start_9_OG [0x09f]						
		AOCU_NM	5	02-76	00 b4 b5 24 8b			
2012/10/30	12:58:00.0	AOCS_Ore-point_Start_10_OG [0x0a0]						
		AOCU_NM	5	02-76	00 d6 36 48 72			
2012/10/30	13:08:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]						
		AOCU_NM	5	02-76	00 29 ca b7 8e			
2012/10/30	13:18:00.0	AOCS_Ore-point_Start_12_OG [0x0a2]						
		AOCU_NM	5	02-76	00 4b 4b db 75			
2012/10/30	13:34:00.0	AOCS_Ore-point_Start_13_OG [0x0a3]						
		AOCU_NM	5	02-76	00 53 a5 00 00			
2012/10/30	13:50:00.0	AOCS_Ore-point_Start_14_OG [0x0a4]						
		AOCU_NM	5	02-76	00 4b 4b 24 8b			
2012/10/30	14:06:00.0	AOCS_Ore-point_Start_15_OG [0x0a5]						
		AOCU_NM	5	02-76	00 29 db 48 72			
2012/10/30	14:30:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]						
		AOCU_NM	5	02-76	02 00 00 00 00			
2012/10/31	10:50:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]						
		AOCU_NM	5	02-76	01 00 00 00 00			
2012/10/31	14:30:00.0	AOCS_Ore-point_Start_18_OG [0x0a8]						
		AOCU_NM	5	02-76	03 00 00 00 00			
2012/10/31	18:00:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]						
		AOCU_NM	5	02-76	02 00 00 00 00			
2012/11/01	09:40:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/11/01	09:41:00.0	AOCS_Ore-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 00 00 00 00			