

# XRT Timeline to be uploaded on 2012/02/04

Period: 2012/02/04 10:29:00 - 2012/02/09 10:17:00

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## Normal mode

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XOB #1881: CME watch - 4x4 - AEC 2 - Ti/poly													
Term	Pointing (x, y)							Comment					
02/04 10:42:00 - 02/04 10:48:54	Fixed ( -920.0, -15.0)	# OP start + 10min, HOP 79, E-W irradiance scan.											
02/04 10:52:00 - 02/04 10:58:54	Fixed ( -721.0, -15.0)												
02/04 11:02:00 - 02/04 11:08:54	Fixed ( -522.0, -15.0)												
02/04 11:12:00 - 02/04 11:18:54	Fixed ( -323.0, -15.0)												
02/04 11:22:00 - 02/04 11:28:54	Fixed ( -124.0, -15.0)												
02/04 11:42:00 - 02/04 11:48:54	Fixed ( 273.0, -15.0)												
02/04 11:52:00 - 02/04 11:58:54	Fixed ( 472.0, -15.0)												
02/04 12:02:00 - 02/04 12:08:54	Fixed ( 671.0, -15.0)												
02/04 12:12:00 - 02/04 12:18:54	Fixed ( 871.0, -15.0)												
02/04 12:22:00 - 02/04 12:28:54	Fixed ( -914.0, -225.0)												
02/04 12:32:00 - 02/04 12:38:54	Fixed ( -759.0, -203.0)												
02/04 12:42:00 - 02/04 12:48:54	Fixed ( -580.0, -185.0)												
02/04 12:52:00 - 02/04 12:58:54	Fixed ( -396.0, -173.0)												
02/04 13:02:00 - 02/04 13:08:54	Fixed ( -206.0, -166.0)												
02/04 13:12:00 - 02/04 13:18:54	Fixed ( -11.0, -164.0)												
02/04 13:22:00 - 02/04 13:28:54	Fixed ( 178.0, -166.0)												
02/04 13:32:00 - 02/04 13:38:54	Fixed ( 368.0, -173.0)												
02/04 13:42:00 - 02/04 13:48:54	Fixed ( 552.0, -185.0)												
02/04 13:52:00 - 02/04 13:58:54	Fixed ( 731.0, -203.0)												
02/04 14:02:00 - 02/04 14:08:54	Fixed ( 900.0, -240.0)	* HOP 79, final pointing.											
<b>PROG= 12 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 120.0sec													
└─ Seqn= 57 1-time(s) 4.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1726: Synoptic 9 Filter- 2x2 Q98 Shorter exp 2 loop													
Term	Pointing (x, y)							Comment					
02/04 11:32:00 - 02/04 11:38:54	Fixed ( 74.0, -15.0)												
<b>PROG= 01 2-time(s)</b>													
└─ Subr= 1 1-time(s) 180.0sec													
└─ Seqn= 82 1-time(s) 25.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 98 1-time(s) 25.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 74 1-time(s) 25.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 99 1-time(s) 25.0sec													
	C-poly/Open	C-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	C-poly/Open	C-poly/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 24 1-time(s) 25.0sec													
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 80 1-time(s) 4.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 360.0sec													
└─ Seqn= 85 1-time(s) 4.0sec													
	med-Al/Open	med-Al/Open	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 71 1-time(s) 4.0sec													
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 67 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 4 1-time(s) 4.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #18AE: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 150s cad													
Term	Pointing (x, y)							Comment					
02/04 14:12:00 - 02/04 17:59:54	Track ( 563.8, 357.1) <sup>Ⓜ 02/04 14:09:00</sup>	* Track AR 11410.											
02/04 18:13:00 - 02/05 06:18:24	Track ( 592.0, 354.8) <sup>Ⓜ 02/04 18:10:00</sup>	# Cont.											
02/05 06:31:30 - 02/05 17:41:30	Track ( 672.8, 346.9) <sup>Ⓜ 02/05 06:28:30</sup>	# Cont.											
<b>PROG= 13 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 19 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 96 4-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Subr= 2	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec	
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	1-time(s)		2.0sec											
	Seqn= 62		15-time(s)		150.0sec									
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec	
	thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec	
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec		
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec		
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec		
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec		
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval		

**XOB #18A8: Synoptic Q95 2x2 - Al/mesh(16/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 -1x1 2048x512) + Ti-poly(33/2048) + Thin-Be(18**

Term	Pointing (x, y)	Comment
02/04 18:03:00 - 02/04 18:09:54	Fixed ( 0.0, 0.0)	synoptic
02/05 06:21:30 - 02/05 06:28:24	Fixed ( 0.0, 0.0)	synoptic, shifted 18.5 min
02/05 18:08:00 - 02/05 18:14:54	Fixed ( 0.0, 0.0)	synoptic, shifted 5.0 min
02/06 05:55:00 - 02/06 06:01:54	Fixed ( 0.0, 0.0)	synoptic, shifted -8.0 min
02/06 17:57:30 - 02/06 18:04:24	Fixed ( 0.0, 0.0)	synoptic, shifted -5.5 min
02/07 06:03:00 - 02/07 06:09:54	Fixed ( 0.0, 0.0)	synoptic

**PROG= 05 1-time(s)**

Subr= 1	1-time(s)		12.0sec										
	Seqn= 7		1-time(s)		4.0sec								
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 5	1-time(s)		2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
Subr= 8	1-time(s)		4.0sec										
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 3	1-time(s)		2.0sec										
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 4	1-time(s)		2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

**XOB #18AF: AR Standard-A(Filter-Ratio) with PFB, shorter thin-Be, thick Al and Al/Poly context, 384x384 at 1064 1048 (all), 4min cad**

Term	Pointing (x, y)	Comment
02/05 18:18:00 - 02/06 05:51:54	Track ( 741.2, 338.3) @ 02/05 18:15:00	# Cont.
02/06 06:05:00 - 02/06 09:59:54	Track ( 800.2, 328.8) @ 02/06 06:02:00	# Cont.
02/06 16:03:00 - 02/06 17:54:24	Track ( 842.1, 320.2) @ 02/06 16:00:00	* Track AR 11410.
02/06 18:07:30 - 02/07 05:59:54	Track ( 849.9, 318.4) @ 02/06 18:04:30	# Cont.
02/07 06:13:00 - 02/07 10:41:00	Fixed ( 878.2, 334.0)	# AR 11410, fixed pointing at W limb.

**PROG= 04 Inf.-time(s)**

Subr= 1	1-time(s)		2.0sec										
	Seqn= 19		1-time(s)		2.0sec								
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Subr= 96	4-time(s)		2.0sec										
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Subr= 2	1-time(s)		2.0sec										
	Seqn= 62		11-time(s)		240.0sec								
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	15.0sec	
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec	
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	15.0sec	
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec	
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	15.0sec	
thin-Be/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec	
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	15.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

**XOB #174E: HOP81 2-filter - Ti/poly 8s, Al/mesh 4s, G-band - 384x384**

Term	Pointing (x, y)	Comment
02/06 10:03:00 - 02/06 15:59:54	Fixed ( -14.0, 885.0)	* HOP 206, panorama at N pole.

**PROG= 07 Inf.-time(s)**

Subr= 1	1-time(s)	2.0sec										
└ Seqn= 51	1-time(s)	2.0sec										
└└ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 2	30-time(s)	2.0sec										
└ Seqn= 58	2-time(s)	30.0sec										
└└ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└└ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Flare mode

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#### XOB #18C2: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops

Term	Pointing (x, y)	Comment
02/04 10:42:00 - 02/04 10:48:54	Fixed ( -920.0, -15.0)	# OP start + 10min, HOP 79, E-W irradiance scan.
02/04 10:52:00 - 02/04 10:58:54	Fixed ( -721.0, -15.0)	
02/04 11:02:00 - 02/04 11:08:54	Fixed ( -522.0, -15.0)	
02/04 11:12:00 - 02/04 11:18:54	Fixed ( -323.0, -15.0)	
02/04 11:22:00 - 02/04 11:28:54	Fixed ( -124.0, -15.0)	
02/04 11:42:00 - 02/04 11:48:54	Fixed ( 273.0, -15.0)	
02/04 11:52:00 - 02/04 11:58:54	Fixed ( 472.0, -15.0)	
02/04 12:02:00 - 02/04 12:08:54	Fixed ( 671.0, -15.0)	
02/04 12:12:00 - 02/04 12:18:54	Fixed ( 871.0, -15.0)	
02/04 12:22:00 - 02/04 12:28:54	Fixed ( -914.0, -225.0)	
02/04 12:32:00 - 02/04 12:38:54	Fixed ( -759.0, -203.0)	
02/04 12:42:00 - 02/04 12:48:54	Fixed ( -580.0, -185.0)	
02/04 12:52:00 - 02/04 12:58:54	Fixed ( -396.0, -173.0)	
02/04 13:02:00 - 02/04 13:08:54	Fixed ( -206.0, -166.0)	
02/04 13:12:00 - 02/04 13:18:54	Fixed ( -11.0, -164.0)	
02/04 13:22:00 - 02/04 13:28:54	Fixed ( 178.0, -166.0)	
02/04 13:32:00 - 02/04 13:38:54	Fixed ( 368.0, -173.0)	
02/04 13:42:00 - 02/04 13:48:54	Fixed ( 552.0, -185.0)	
02/04 13:52:00 - 02/04 13:58:54	Fixed ( 731.0, -203.0)	
02/04 14:02:00 - 02/04 14:08:54	Fixed ( 900.0, -240.0)	* HOP 79, final pointing.
02/04 14:12:00 - 02/04 17:59:54	Track ( 563.8, 357.1) @ 02/04 14:09:00	* Track AR 11410.
02/04 18:13:00 - 02/05 06:18:24	Track ( 592.0, 354.8) @ 02/04 18:10:00	# Cont.
02/05 06:31:30 - 02/05 17:41:30	Track ( 672.8, 346.9) @ 02/05 06:28:30	# Cont.
02/05 18:18:00 - 02/06 05:51:54	Track ( 741.2, 338.3) @ 02/05 18:15:00	# Cont.
02/06 06:05:00 - 02/06 09:59:54	Track ( 800.2, 328.8) @ 02/06 06:02:00	# Cont.
02/06 10:03:00 - 02/06 15:59:54	Fixed ( -14.0, 885.0)	* HOP 206, panorama at N pole.
02/06 16:03:00 - 02/06 17:54:24	Track ( 842.1, 320.2) @ 02/06 16:00:00	* Track AR 11410.
02/06 18:07:30 - 02/07 05:59:54	Track ( 849.9, 318.4) @ 02/06 18:04:30	# Cont.
02/07 06:13:00 - 02/07 10:41:00	Fixed ( 878.2, 334.0)	# AR 11410, fixed pointing at W limb.

#### PROG= 03 12-time(s)

Subr= 1	45-time(s)	10.0sec										
└ Seqn= 20	1-time(s)	2.0sec										
└└ thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└ Seqn= 63	1-time(s)	2.0sec										
└└ med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└└ Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└ Seqn= 77	1-time(s)	2.0sec										
└└ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	10.0sec										
└ Seqn= 90	1-time(s)	2.0sec										
└└ Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└└ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└└ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

### Active Region Search

\* \* \* \* \*

NOT USED

\* \* \* \* \*

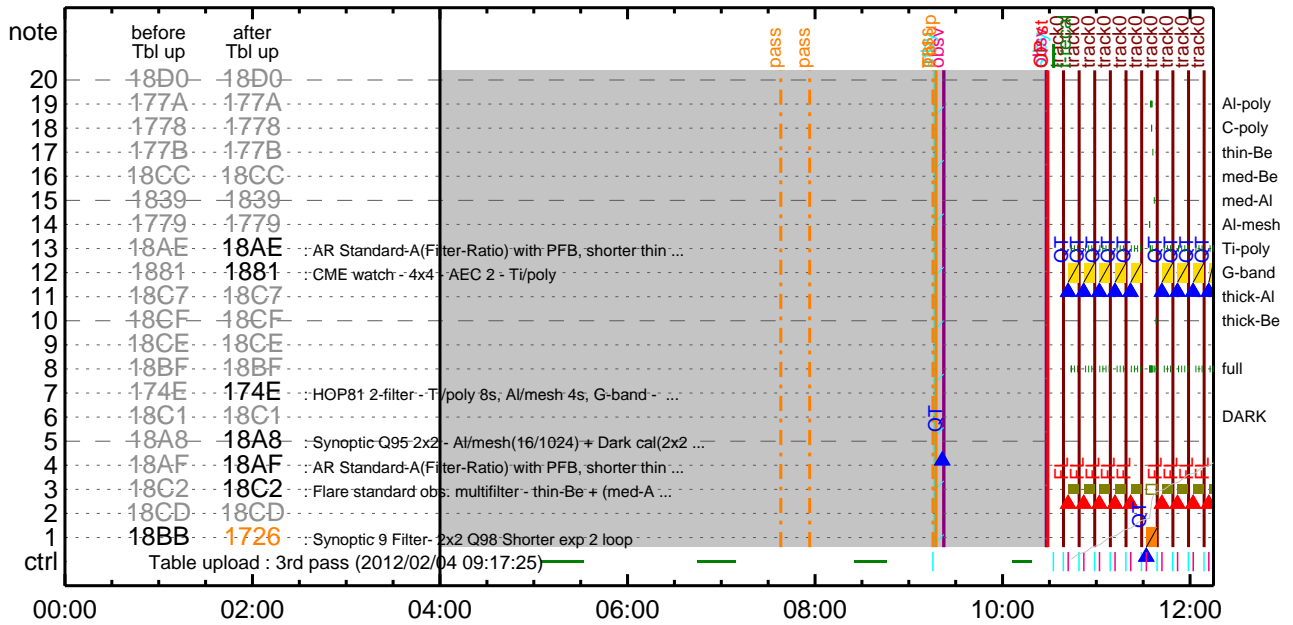
### Flare Detection

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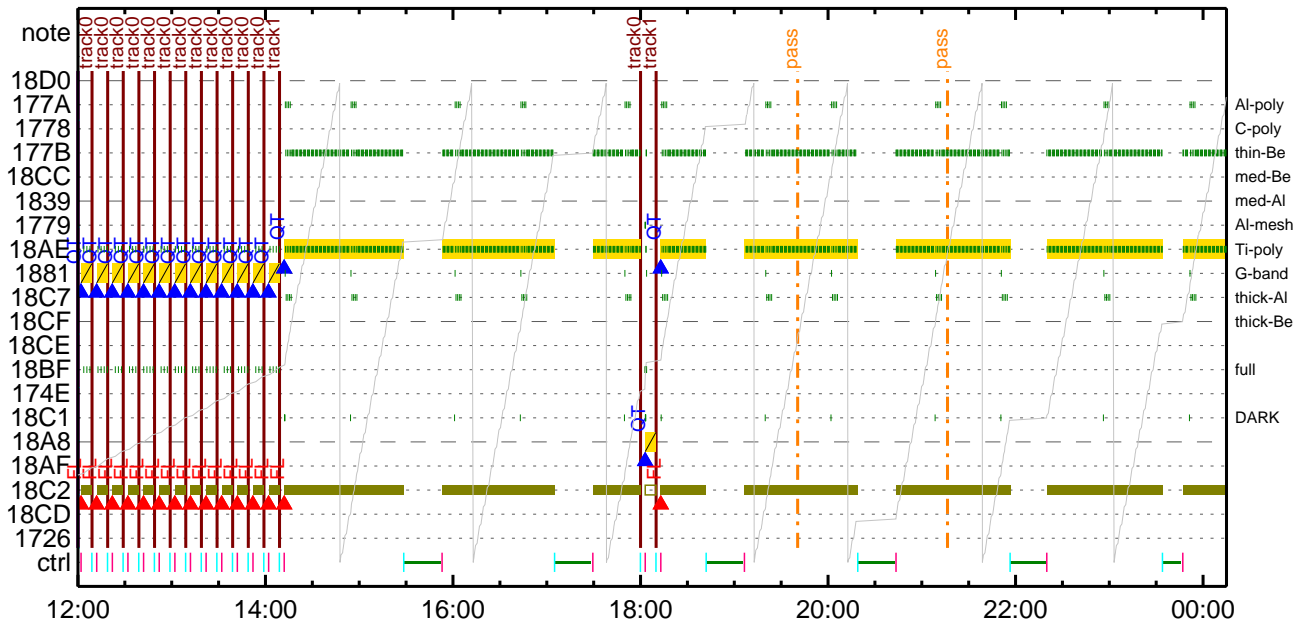
#### FLD Patrol

Term	Pointing (x, y)	Comment									
02/04 11:41:46 - 02/04 18:00:16	Fixed ( 273.0, -15.0)										
02/04 18:12:46 - 02/05 06:18:46	Track ( 592.0, 354.8) @ 02/04 18:10:00	# Cont.									
02/05 06:31:16 - 02/05 18:05:16	Track ( 672.8, 346.9) @ 02/05 06:28:30	# Cont.									
02/05 18:17:46 - 02/06 05:52:16	Track ( 741.2, 338.3) @ 02/05 18:15:00	# Cont.									
02/06 06:04:46 - 02/06 17:54:46	Track ( 800.2, 328.8) @ 02/06 06:02:00	# Cont.									
02/06 18:07:16 - 02/07 06:00:16	Track ( 849.9, 318.4) @ 02/06 18:04:30	# Cont.									
02/07 06:12:46 - 02/09 10:17:00	Fixed ( 878.2, 334.0)	# AR 11410, fixed pointing at W limb.									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

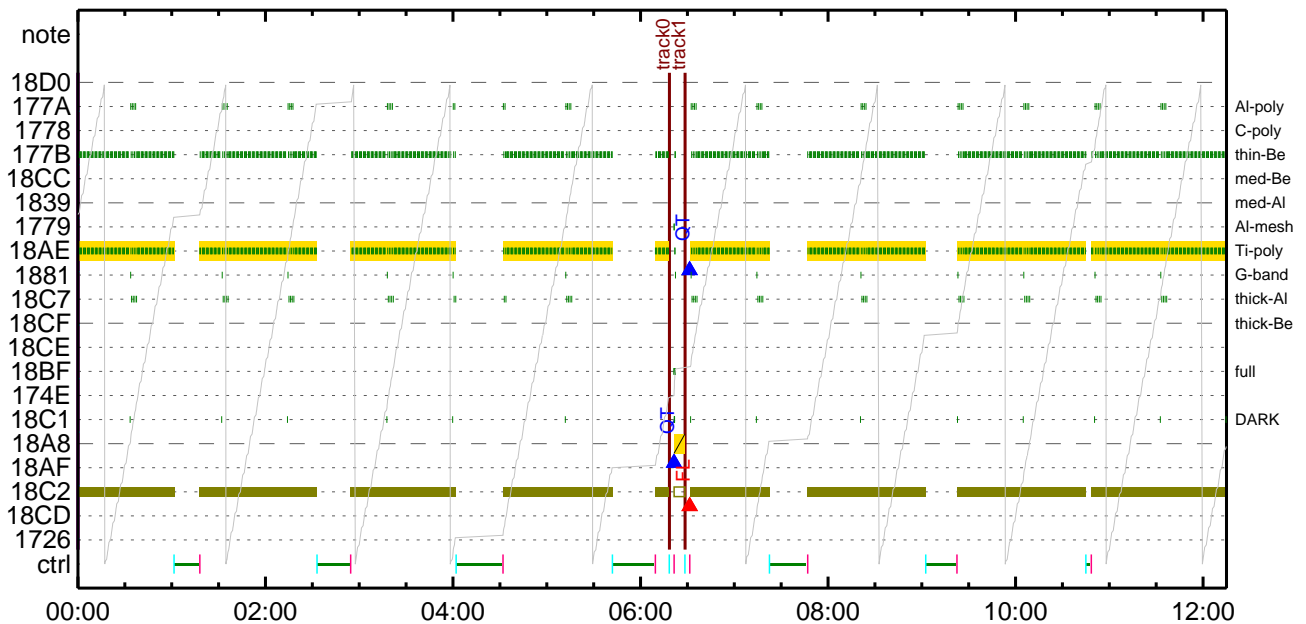
### CMDI #0409 2012/02/04



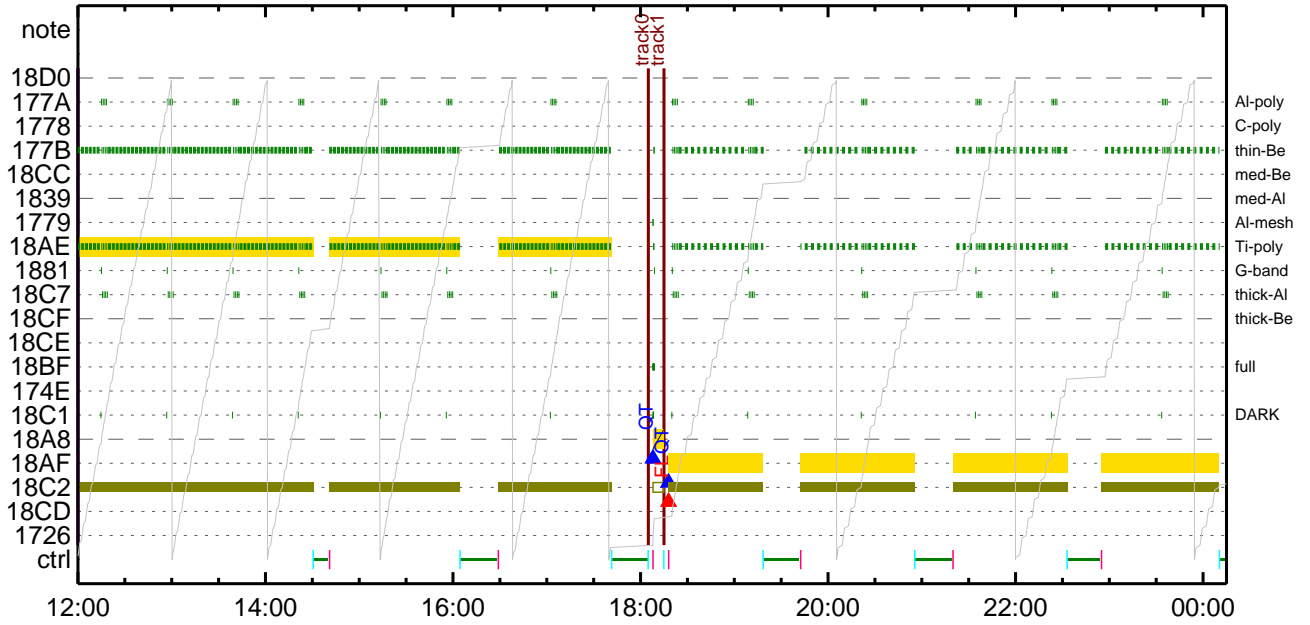
### CMDI #0409 2012/02/04



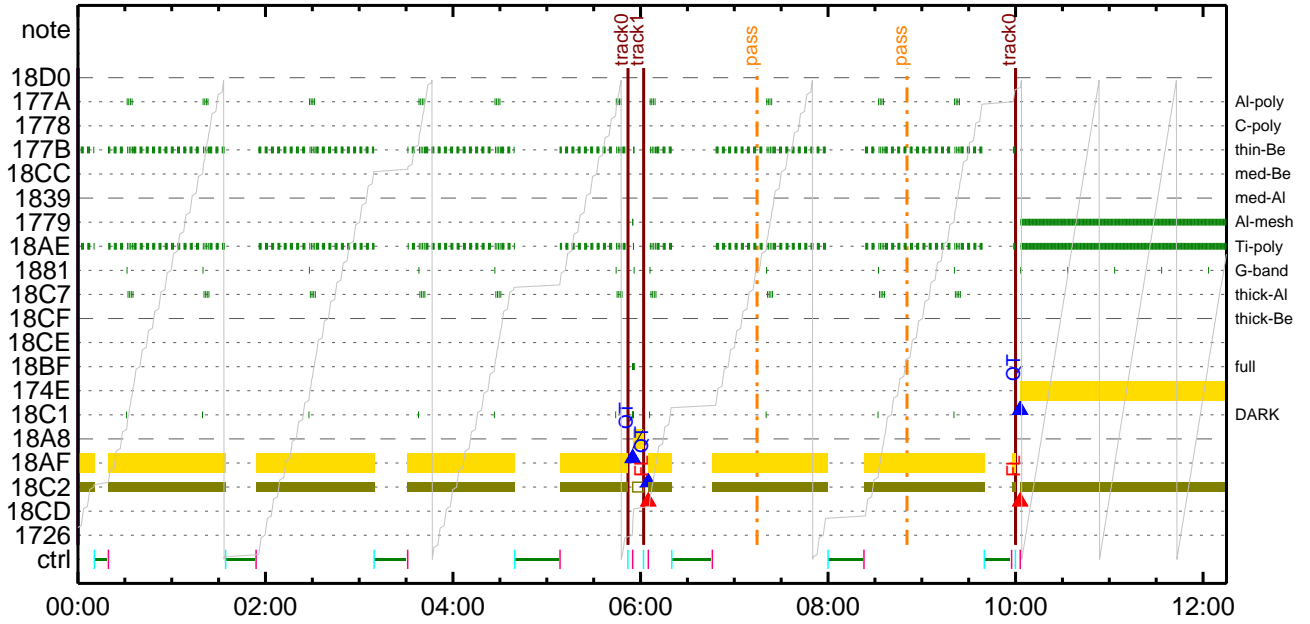
### CMDI #0409 2012/02/05



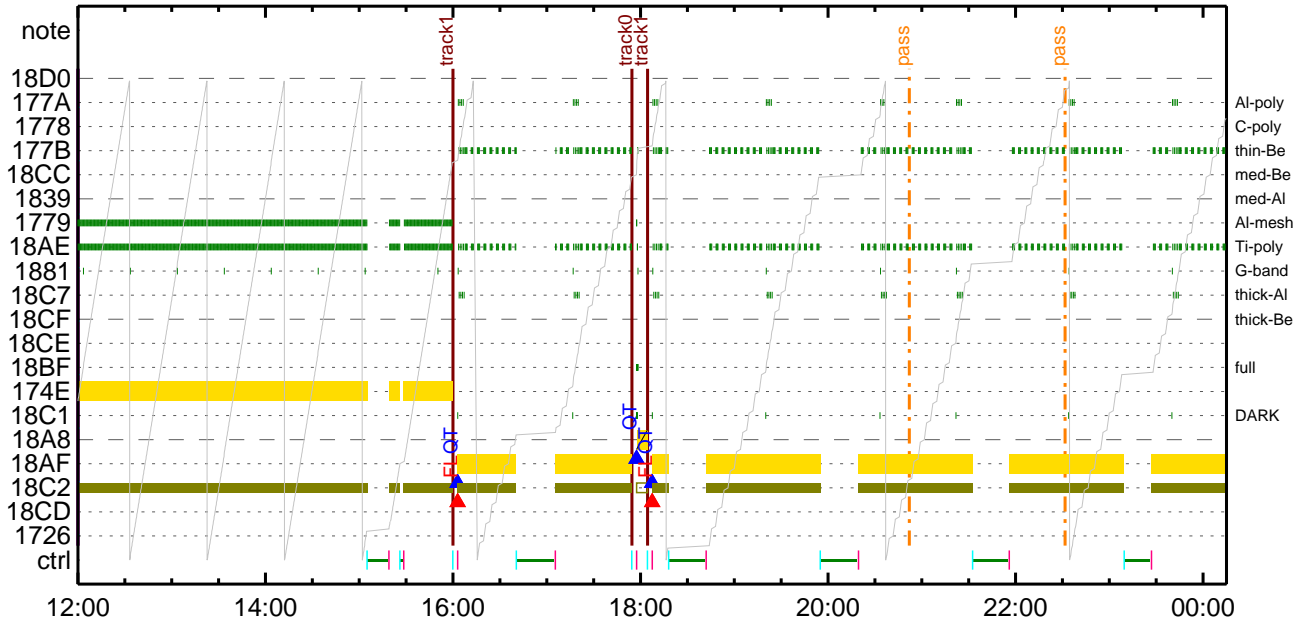
CMDI #0409 2012/02/05



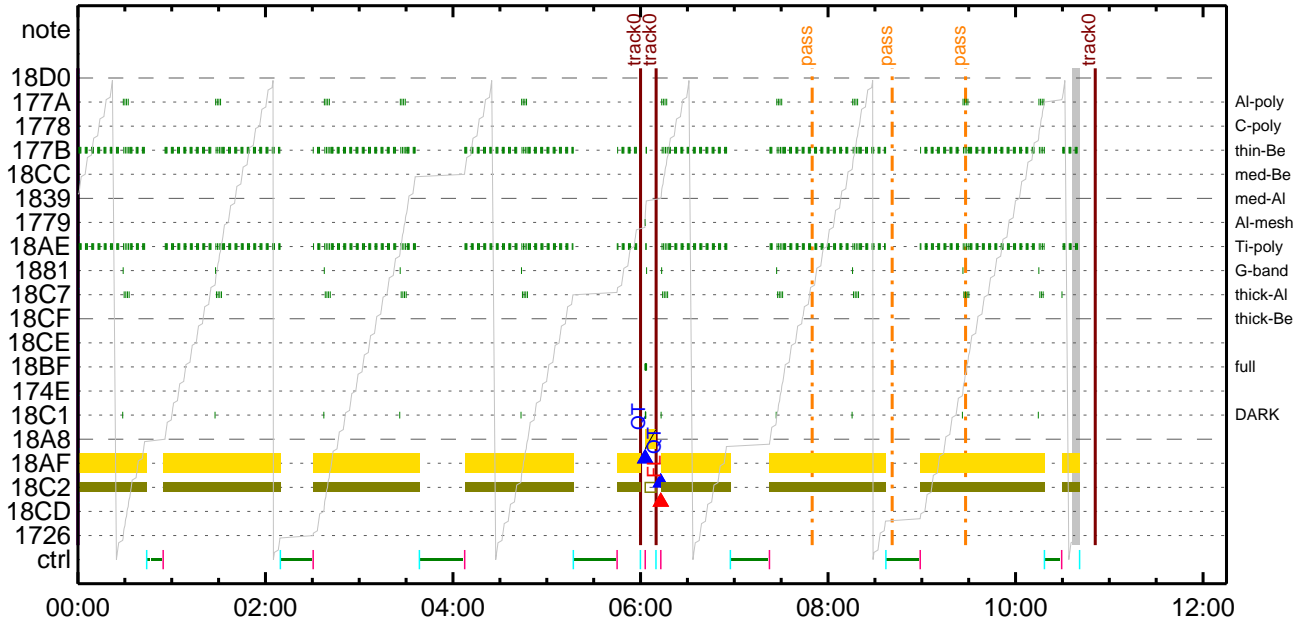
CMDI #0409 2012/02/06



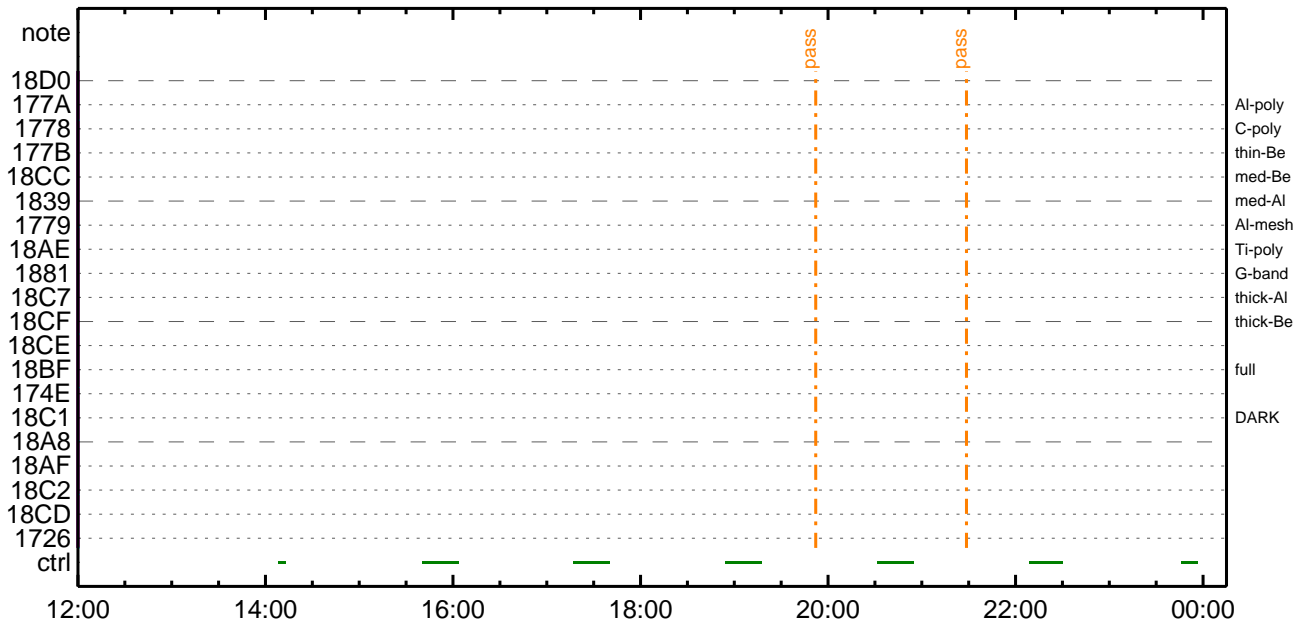
CMDI #0409 2012/02/06



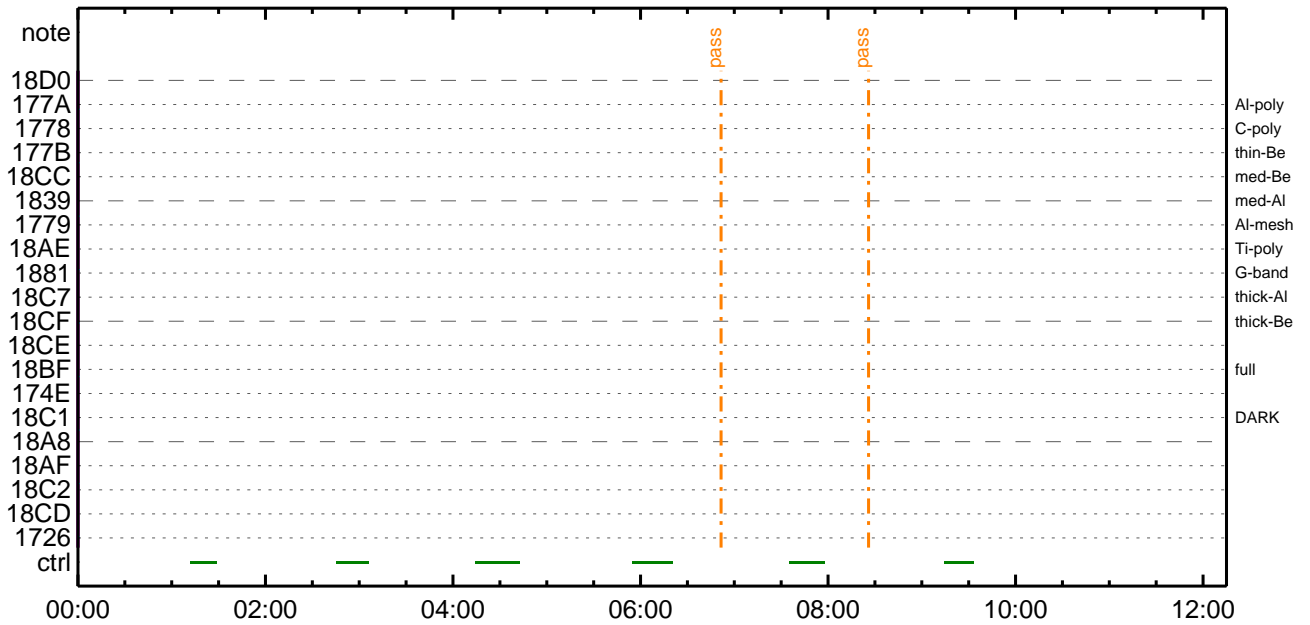
CMDI #0409 2012/02/07



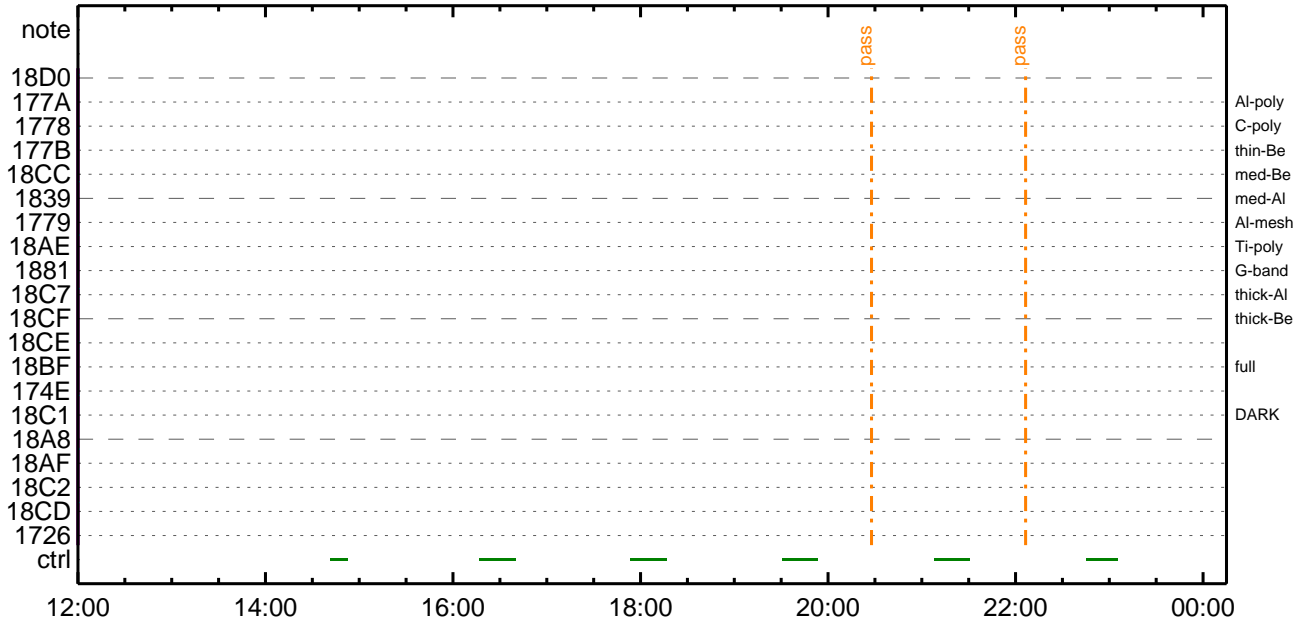
CMDI #0409 2012/02/07



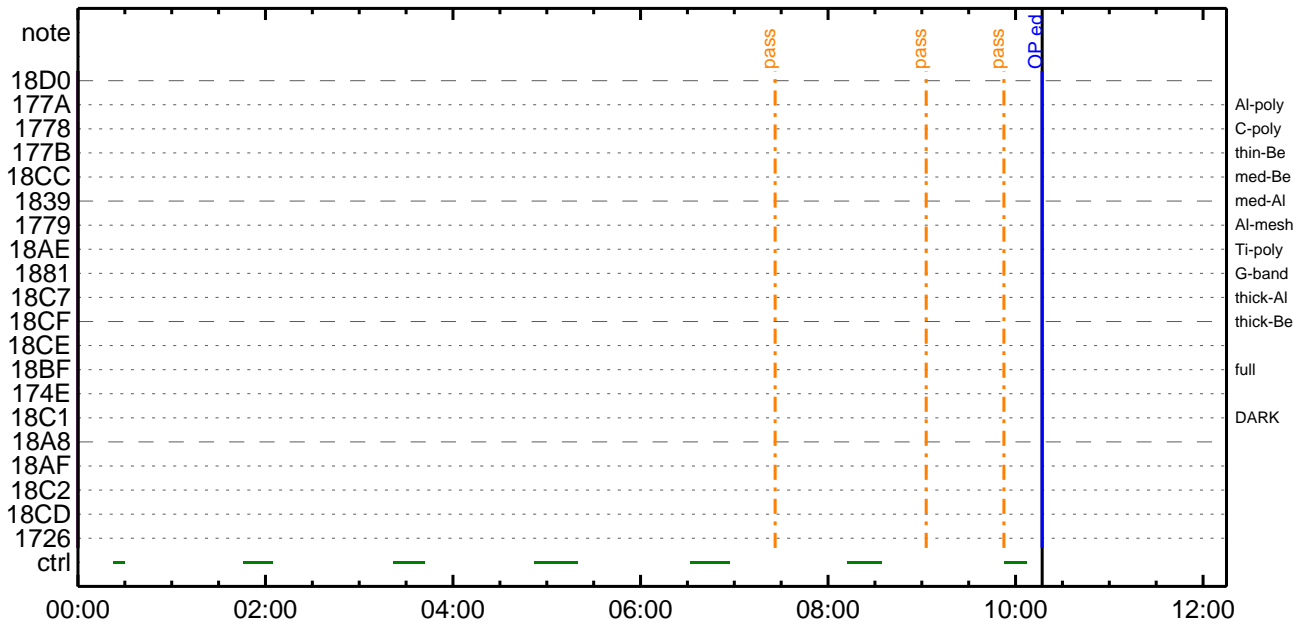
CMDI #0409 2012/02/08



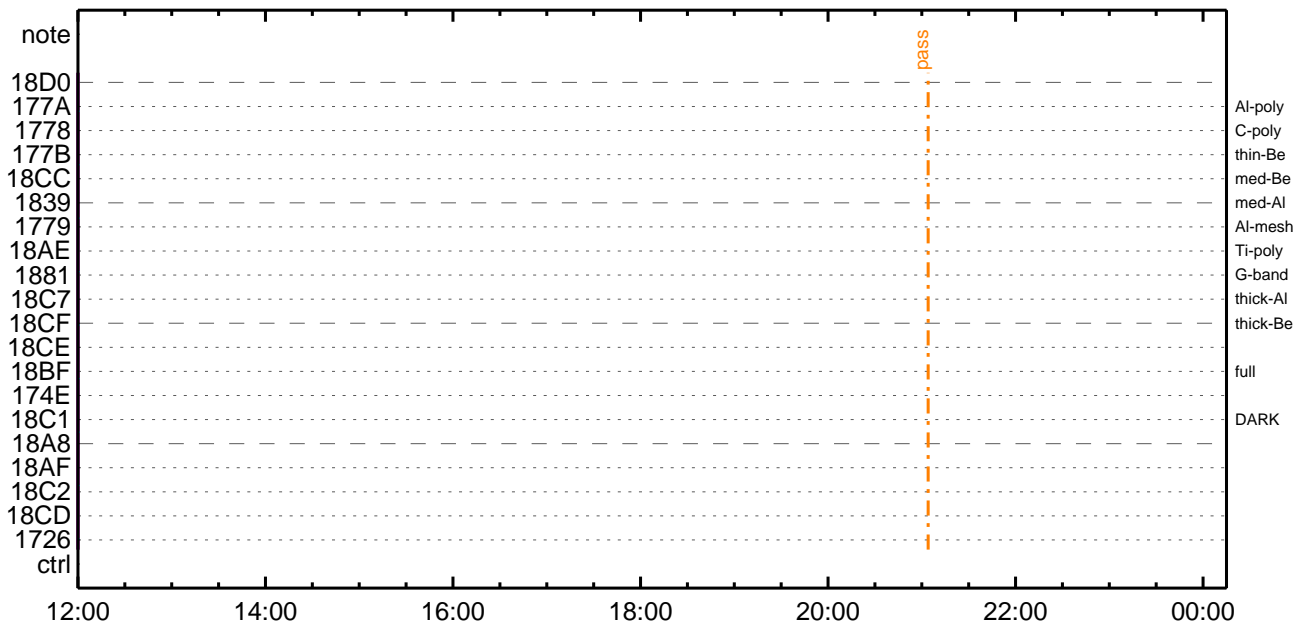
CMDI #0409 2012/02/08



CMDI #0409 2012/02/09



CMDI #0409 2012/02/09







```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-636:OP
0104 ( )
0105 S. OG og-636:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPf°eYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGªî½ª¹ç•ë²îOKªò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGªî½ª¹ç•ë²îOKªò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPªî½ª¹ç•ë²îOKªò³îÇ§
0165 C.
0166 C. ***** °ê²¼ªî½ª¹ç•ë²îOKªò³îÇ§ *****
0167 C. DHUYâ;4YE;ê½½;Yi;4YE;ëªòîãª¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADª-Á÷ç@NGªî½ª¹ç;ç°ê²¼ªî½ª¹ç@ªî½ª¹çªòª³ªë;ç
0180 C. ªªªç;çSETªEDUMªªî½ª¹ç°ª¹çªòª³ªë;ç
0181 C.
0182 C. TIY³YpYóYêªòªî½ª¹ç(UT)
0183 +. TI 2012-02-04 10:24:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2012-02-04 10:24:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2012-02-04 10:24:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```



(a) Spacecraft Operation Procedure (real-commands)

```
main-637 2012-02-04 12:20:56 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿µÁµ°µ»Í×ÁÇµÍYçYÁY×Yí;¼YÉ;ÈÈ%µ•íÉ;ÈÈ¼°ÇÔµ•µ¿¼i¹çµÍ;çÀ®, ùµ¹µèµµµÇÁ+¿®µ•µÈµµµ³µÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 0ms
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-822:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2012-02-04 10:28:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `ûÁîîî»ó¼YµÈÁµ¹µèDCBC•x²è *****
0069 C. (¼á°íYÓYÁYÈYÈYÈYáYçYèµÈ¼µ¼Á»Ûµ¹µè)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** YD¥¹.İ Daily±¿ÍÑµÈ´Øµ¹µèDCBC•x²è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. ;ãLOSÁY$YÁY~¼Á»Û;ã
0080 C.
0081 . C. ***** LOS *****
0082 C.
```



```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-263:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2012-02-04 10:28:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 + DC 07-F0 MDP_XRT_MODE_STBY
0136 BC (c3)
0137 . C. ----- Success Verify ? OK / NG____
0138 C.
0139 C. XRT Obs. Table Upload
0140 . S. RAM ram-291:MDP_OBS_X
0141 ( )
0142 C.
0143 +. DC 07-F0 MDP_DUMP_XRTTBL
0144 BC (84 07 00 00 00 3a d4)
0145 . C. ----- Comparison Check ? OK / ERR ____
0146 C.
0147 C.
0148 +. DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 01 b1 b1 04 04)
0150 + DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 02 b1 b1 08 08)
0152 + DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 03 b1 b1 08 08)
0154 + DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 04 b1 b1 06 06)
0156 + DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 05 85 83 06 06)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 80 80 20 20)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 80 80 20 08)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 80 80 08 20)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 85 83 06 06)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0a 85 83 08 08)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0f 80 80 06 06)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 10 80 80 08 08)
0172 + DC 07-F0 MDP_XRT_FLD_ENA
0173 BC (d8)
0174 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0175 BC (c8)
0176 + DC 07-F0 MDP_XRT_AEC_RESET
0177 BC (d0)
0178 + DC 07-F0 MDP_XRT_ARS_DIS
0179 BC (d5)
0180 + DC 07-F0 MDP_XRT_FLD_RESET
0181 BC (da)
0182 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0183 BC (c4 05)
0184 . C. ----- Success Verify ? OK / NG ____
0185 C.
0186 C.
0187 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0188 C.
0189 +. DC 07-F0 MDP_XRT_MODE_OBSV
0190 BC (c2)
0191 +. TI 2012-02-04 10:28:02.0
0192 DC 07-F0 MDP_XRT_MODE_OBSV
0193 BC (c2)

```

```
0194 . C. ----- Success Verify ?          OK / NG ____
0195 . C.
0196 . C. ***** XRT END *****
0197 . C.
0198 . C. ***** MDP `úÃîñî»ö¼ÿñÊÃðñ¹ñèDCBC•x²è *****
0199 . C. (¼ã°îÿÓÿÃÿÈÿPÿÈÿãÿçÿéñ¼ñ¼¼Ã»Ûñ¹ñè)
0200 . S. DC-BC dcbc-402:DCBC
0201 (MDP_known_event)
0202 . C.
0203 . C.
0204 . C. ***** ÿDÿ¹•Ï Daily±;îÑñÊ´Øñ¹ñèDCBC•x²è *****
0205 . S. DC-BC dcbc-153:DCBC
0206 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0207 . C.
0208 . C.
0209 . C. ;ãLOSÿÃÿSÿÿÃÿ-¼Ã»Û;ã
0210 . C.
0211 . C. ***** LOS *****
0212 . C.
```

Feb 04, 12 12:21

XRT\_OGLIST\_0409.chk

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\*\*\* OP Sequence for XRT \*\*\*

2012/02/04	10:32:30.0	XRT_CTRL_MANU_447_OG [0x1bf]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/04	10:32:40.0	XRT_FOCUS_RECALIBRATE_428_OG [0x1ac]							
		XRT_FOCUS_RECAL	2	07-F8	78	00			
2012/02/04	10:36:40.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/02/04	10:38:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/04	10:39:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	01	58	51	ca
2012/02/04	10:41:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/02/04	10:41:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/02/04	10:41:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/02/04	10:41:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/02/04	10:41:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/04	10:41:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/04	10:41:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2012/02/04	10:41:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2012/02/04	10:42:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/04	10:48:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/04	10:49:00.5	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	01	58	40	18
2012/02/04	10:51:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/02/04	10:51:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/02/04	10:51:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/02/04	10:51:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/02/04	10:51:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/04	10:51:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/04	10:51:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2012/02/04	10:51:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2012/02/04	10:52:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/04	10:58:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/04	10:59:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	01	58	2e	66
2012/02/04	11:01:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/02/04	11:01:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/02/04	11:01:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/02/04	11:01:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/02/04	11:01:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/04	11:01:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/04	11:01:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2012/02/04	11:01:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2012/02/04	11:02:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/04	11:08:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/04	11:09:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	01	58	1c	b4
2012/02/04	11:11:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/02/04	11:11:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/02/04	11:11:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/02/04	11:11:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/02/04	11:11:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/04	11:11:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/04	11:11:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2012/02/04	11:11:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							

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2012/02/04	11:12:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2012/02/04	11:18:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/04	11:19:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/04	11:21:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 01 58 0b	02
2012/02/04	11:21:46.0	XRT_FLD_ENA_435_OG [0x1b3]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/02/04	11:21:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/02/04	11:21:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/02/04	11:21:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/02/04	11:21:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/02/04	11:21:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/02/04	11:21:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2012/02/04	11:22:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2012/02/04	11:28:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/04	11:28:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/04	11:29:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/02/04	11:29:16.0	XRT_FLD_DIS_402_OG [0x192]	AOCU_NM	5	02-76	00 01 58 f9	69
2012/02/04	11:29:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/02/04	11:29:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/02/04	11:31:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/02/04	11:32:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2012/02/04	11:38:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/04	11:39:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/04	11:41:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 01 58 e7	bf
2012/02/04	11:41:46.0	XRT_FLD_ENA_435_OG [0x1b3]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/02/04	11:41:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/02/04	11:41:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/02/04	11:41:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/02/04	11:41:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/02/04	11:41:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/02/04	11:41:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2012/02/04	11:42:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2012/02/04	11:48:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/04	11:49:00.0	AOCS_Ore-point_Start_8_OG [0x09e]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/04	11:51:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 01 58 d6	0d
2012/02/04	11:51:46.0	XRT_FLD_ENA_435_OG [0x1b3]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/02/04	11:51:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/02/04	11:51:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/02/04	11:51:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/02/04	11:51:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/02/04	11:51:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da	
2012/02/04	11:51:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2012/02/04	11:52:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2012/02/04	11:58:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/04	11:59:00.0	AOCS_Ore-point_Start_9_OG [0x09f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/04	12:01:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	AOCU_NM	5	02-76	00 01 58 c4	5b
2012/02/04	12:01:46.0	XRT_FLD_ENA_435_OG [0x1b3]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2012/02/04			MDP_XRT_FLD_ENA	1	07-F0	d8	



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2012/02/04	12:01:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/02/04	12:01:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/02/04	12:01:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/02/04	12:01:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/04	12:01:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2012/02/04	12:01:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2012/02/04	12:02:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/04	12:08:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/04	12:09:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00	01	58	b2 98
2012/02/04	12:11:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2012/02/04	12:11:46.0	XRT_FLD_ENA_435_OG [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/02/04	12:11:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/02/04	12:11:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/02/04	12:11:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/02/04	12:11:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/04	12:11:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2012/02/04	12:11:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2012/02/04	12:12:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/04	12:18:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/04	12:19:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00	14	00	51 3f
2012/02/04	12:21:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2012/02/04	12:21:46.0	XRT_FLD_ENA_435_OG [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/02/04	12:21:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/02/04	12:21:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/02/04	12:21:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/02/04	12:21:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/04	12:21:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2012/02/04	12:21:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2012/02/04	12:22:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/04	12:28:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/04	12:29:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00	12	0c	43 74
2012/02/04	12:31:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2012/02/04	12:31:46.0	XRT_FLD_ENA_435_OG [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/02/04	12:31:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/02/04	12:31:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/02/04	12:31:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/02/04	12:31:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/04	12:31:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2012/02/04	12:31:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2012/02/04	12:32:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/04	12:38:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/04	12:39:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00	10	72	33 8d
2012/02/04	12:41:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2012/02/04	12:41:46.0	XRT_FLD_ENA_435_OG [0x1b3]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/02/04	12:41:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/02/04	12:41:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/02/04	12:41:52.0	XRT_ARS_DIS_431_OG [0x1af]							

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2012/02/04	12:41:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_ARS_DIS	1	07-F0	d5			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/04	12:41:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2012/02/04	12:41:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2012/02/04	12:42:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/04	12:48:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/04	12:49:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
			AOCU_NM	5	02-76	00	0f 64 23 33		
2012/02/04	12:51:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
			XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00		
2012/02/04	12:51:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/02/04	12:51:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/02/04	12:51:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/02/04	12:51:52.0	XRT_ARS_DIS_431_OG [0x1af]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/02/04	12:51:54.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/04	12:51:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2012/02/04	12:51:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2012/02/04	12:52:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/04	12:58:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/04	12:59:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
			AOCU_NM	5	02-76	00	0e c0 12 4d		
2012/02/04	13:01:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
			XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00		
2012/02/04	13:01:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/02/04	13:01:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/02/04	13:01:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/02/04	13:01:52.0	XRT_ARS_DIS_431_OG [0x1af]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/02/04	13:01:54.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/04	13:01:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2012/02/04	13:01:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2012/02/04	13:02:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/04	13:08:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/04	13:09:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
			AOCU_NM	5	02-76	00	0e 97 00 fd		
2012/02/04	13:11:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
			XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00		
2012/02/04	13:11:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/02/04	13:11:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/02/04	13:11:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/02/04	13:11:52.0	XRT_ARS_DIS_431_OG [0x1af]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/02/04	13:11:54.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/04	13:11:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2012/02/04	13:11:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2012/02/04	13:12:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/04	13:18:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/04	13:19:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
			AOCU_NM	5	02-76	00	0e c0 f0 32		
2012/02/04	13:21:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
			XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00		
2012/02/04	13:21:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/02/04	13:21:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/02/04	13:21:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/02/04	13:21:52.0	XRT_ARS_DIS_431_OG [0x1af]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/02/04	13:21:54.0	XRT_FLD_RESET_412_OG [0x19c]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/04	13:21:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		

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2012/02/04	13:21:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2012/02/04	13:22:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/04	13:28:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/04	13:29:00.0	AOCS_OrE-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00	0f 64 df 4c			
2012/02/04	13:31:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00			
2012/02/04	13:31:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/02/04	13:31:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/02/04	13:31:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/02/04	13:31:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/04	13:31:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/04	13:31:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2012/02/04	13:31:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2012/02/04	13:32:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/04	13:38:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/04	13:39:00.0	AOCS_OrE-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00	10 72 ce f2			
2012/02/04	13:41:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00			
2012/02/04	13:41:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/02/04	13:41:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/02/04	13:41:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/02/04	13:41:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/04	13:41:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/04	13:41:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2012/02/04	13:41:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2012/02/04	13:42:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/04	13:48:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/04	13:49:00.0	AOCS_OrE-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00	12 0c bf 03			
2012/02/04	13:51:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00			
2012/02/04	13:51:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/02/04	13:51:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/02/04	13:51:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/02/04	13:51:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/04	13:51:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/04	13:51:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2012/02/04	13:51:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2012/02/04	13:52:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/04	13:58:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/04	13:59:00.0	AOCS_OrE-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00	15 58 b0 00			
2012/02/04	14:01:26.0	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00			
2012/02/04	14:01:46.0	XRT_FLD_ENA_435_OG [0x1b3]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/02/04	14:01:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/02/04	14:01:50.0	XRT_AEC_RESET_443_OG [0x1bb]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/02/04	14:01:52.0	XRT_ARS_DIS_431_OG [0x1af]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/04	14:01:54.0	XRT_FLD_RESET_412_OG [0x19c]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/04	14:01:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2012/02/04	14:01:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2012/02/04	14:02:00.0	XRT_CTRL_AUTO_406_OG [0x196]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/04	14:08:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							

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2012/02/04	14:09:00.5	AOCS_Orе-point_Start_22_OG [0x0ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			AOCU_NM	5	02-76	01 00 00 00 00			
2012/02/04	14:11:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/02/04	14:11:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/02/04	14:11:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/02/04	14:11:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/02/04	14:11:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/02/04	14:11:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/04	14:11:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d			
2012/02/04	14:11:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2012/02/04	14:12:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/04	15:28:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/04	15:28:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/04	15:28:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/02/04	15:31:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/02/04	15:52:00.0	XRT_Custom_418_OG [0x1a2]							
2012/02/04	15:53:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/04	17:05:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/04	17:05:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/04	17:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/02/04	17:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/02/04	17:28:30.0	XRT_Custom_418_OG [0x1a2]							
2012/02/04	17:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/04	17:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/04	17:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2012/02/04	18:00:00.0	AOCS_Orе-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00 00 00 00 00			
2012/02/04	18:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2012/02/04	18:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2012/02/04	18:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/02/04	18:02:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05			
2012/02/04	18:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/04	18:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/04	18:10:00.0	AOCS_Orе-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	01 00 00 00 00			
2012/02/04	18:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2012/02/04	18:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2012/02/04	18:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2012/02/04	18:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2012/02/04	18:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2012/02/04	18:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/04	18:12:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d			
2012/02/04	18:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2012/02/04	18:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/04	18:42:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2012/02/04	18:42:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da			
2012/02/04	18:42:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2012/02/04	18:45:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2012/02/04	19:05:30.0	XRT_Custom_418_OG [0x1a2]							
2012/02/04	19:06:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2012/02/04	20:19:00.0	XRT_CTRL_MANU_408_OG [0x198]							

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2012/02/04	20:19:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/04	20:19:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/04	20:22:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/04	20:42:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/04	20:43:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/04	21:56:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/04	21:56:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/04	21:56:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/04	21:59:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/04	22:19:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/04	22:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/04	23:34:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/04	23:34:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/04	23:34:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/04	23:37:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/04	23:46:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/04	23:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/05	01:01:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/05	01:01:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/05	01:01:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/05	01:04:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/05	01:17:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/05	01:18:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/05	02:33:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/05	02:33:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/05	02:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/05	02:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/05	02:53:30.5	XRT_Custom_418_OG [0x1a2]				
2012/02/05	02:54:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/05	04:02:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/05	04:02:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/05	04:02:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/05	04:05:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/05	04:31:00.5	XRT_Custom_418_OG [0x1a2]				
2012/02/05	04:32:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/05	05:42:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/05	05:42:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/05	05:42:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/05	05:45:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/05	06:08:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/05	06:09:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/05	06:18:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/05	06:18:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/02/05	06:18:30.5	AOCs_OrE-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00 00 00 00 00
2012/02/05	06:18:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/02/05	06:18:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/02/05	06:18:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/05	06:21:28.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/02/05	06:21:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/05	06:28:24.0	XRT_CTRL_MANU_439_OG [0x1b7]				

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2012/02/05	06:28:30.0	AOCS_ORe-point_Start_22_OG [0x0ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			AOCU_NM	5	02-76	01 00 00 00 00
2012/02/05	06:30:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/02/05	06:31:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/02/05	06:31:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/02/05	06:31:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/02/05	06:31:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/05	06:31:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/05	06:31:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2012/02/05	06:31:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/02/05	06:31:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/05	07:22:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/05	07:22:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/05	07:22:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/05	07:25:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/05	07:46:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/05	07:47:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/05	09:02:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/05	09:02:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/05	09:02:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/05	09:05:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/05	09:21:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/05	09:22:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/05	10:45:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/05	10:45:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/05	10:45:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/05	10:47:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/05	10:48:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/05	10:48:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/05	14:30:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/05	14:30:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/05	14:30:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/05	14:33:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/05	14:40:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/05	14:41:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/05	16:04:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/05	16:04:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/05	16:04:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/05	16:07:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/05	16:28:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/05	16:29:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/05	17:41:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/05	17:41:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/05	17:41:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/05	17:44:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/05	18:04:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/05	18:04:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/02/05	18:05:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00 00 00 00 00
2012/02/05	18:05:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/02/05	18:05:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]				

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2012/02/05	18:05:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
			MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/05	18:07:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/02/05	18:08:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/05	18:14:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/05	18:15:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	01 00 00 00 00
2012/02/05	18:17:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/02/05	18:17:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/02/05	18:17:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/02/05	18:17:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/02/05	18:17:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/05	18:17:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/05	18:17:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2012/02/05	18:17:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/02/05	18:18:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/05	19:18:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/05	19:18:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/05	19:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/05	19:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/05	19:41:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/05	19:42:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/05	20:55:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/05	20:55:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/05	20:55:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/05	20:58:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/05	21:19:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/05	21:20:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/05	22:33:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/05	22:33:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/05	22:33:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/05	22:36:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/05	22:54:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/05	22:55:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/06	00:10:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/06	00:10:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/06	00:10:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/06	00:13:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/06	00:18:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/06	00:19:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/06	01:34:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/06	01:34:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/06	01:34:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/06	01:37:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/06	01:53:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/06	01:54:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/06	03:09:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/06	03:09:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/06	03:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/06	03:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/06	03:30:00.5	XRT_Custom_418_OG [0x1a2]				

2012/02/06	03:31:00.5	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/06	04:39:30.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/06	04:39:32.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da	
2012/02/06	04:39:34.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/02/06	04:42:44.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/02/06	05:07:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/06	05:08:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/06	05:51:54.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/06	05:51:56.0	XRT_FOCUS_POSITION_401_OG [0x191] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2012/02/06	05:52:00.0	AOCS_Ore-point_Start_23_OG [0x0ad] AOCU_NM	5	02-76	00 00 00 00 00	
2012/02/06	05:52:16.0	XRT_FLD_DIS_402_OG [0x192] MDP_XRT_FLD_DIS	1	07-F0	d9	
2012/02/06	05:52:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2012/02/06	05:52:20.0	XRT_ARS_DIS_438_OG [0x1b6] MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/02/06	05:54:58.0	XRT_QT_PROG_SET_425_OG [0x1a9] MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2012/02/06	05:55:00.0	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/06	06:01:54.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/06	06:02:00.0	AOCS_Ore-point_Start_22_OG [0x0ac] AOCU_NM	5	02-76	01 00 00 00 00	
2012/02/06	06:04:26.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/02/06	06:04:46.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/02/06	06:04:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2012/02/06	06:04:50.5	XRT_AEC_RESET_443_OG [0x1bb] MDP_XRT_AEC_RESET	1	07-F0	d0	
2012/02/06	06:04:52.5	XRT_ARS_DIS_431_OG [0x1af] MDP_XRT_ARS_DIS	1	07-F0	d5	
2012/02/06	06:04:54.5	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da	
2012/02/06	06:04:56.5	XRT_QT_PROG_SET_445_OG [0x1bd] MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2012/02/06	06:04:58.5	XRT_FL_PROG_SET_414_OG [0x19e] MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2012/02/06	06:05:00.5	XRT_CTRL_AUTO_406_OG [0x196] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/06	06:20:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/06	06:20:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da	
2012/02/06	06:20:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/02/06	06:23:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/02/06	06:45:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/06	06:46:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/06	08:00:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/06	08:00:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da	
2012/02/06	08:00:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/02/06	08:03:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/02/06	08:22:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/06	08:23:00.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/06	09:40:00.0	XRT_CTRL_MANU_408_OG [0x198] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/06	09:40:02.0	XRT_FLD_RESET_412_OG [0x19c] MDP_XRT_FLD_RESET	1	07-F0	da	
2012/02/06	09:40:04.0	XRT_PREFLR_STRT_422_OG [0x1a6] MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2012/02/06	09:43:14.0	XRT_PREFLR_STOP_424_OG [0x1a8] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2012/02/06	09:56:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/06	09:57:30.0	XRT_CTRL_AUTO_419_OG [0x1a3] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2012/02/06	09:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2012/02/06	10:00:00.0	AOCS_Ore-point_Start_24_OG [0x0ae] AOCU_NM	5	02-76	00 b1 59 01 3f	
2012/02/06	10:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2012/02/06	10:02:46.0	XRT_FLD_ENA_435_OG [0x1b3] MDP_XRT_FLD_ENA	1	07-F0	d8	
2012/02/06	10:02:48.0	XRT_FLRCTRL_ENA_403_OG [0x193]				



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2012/02/06	10:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/02/06	10:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/06	10:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/06	10:02:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2012/02/06	10:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2012/02/06	10:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/06	15:05:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/06	15:05:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/06	15:05:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/06	15:08:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/06	15:18:00.0	XRT_Custom_418_OG [0x1a2]								
2012/02/06	15:19:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/06	15:26:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/06	15:26:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/06	15:26:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/06	15:27:30.0	XRT_Custom_418_OG [0x1a2]								
2012/02/06	15:28:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/06	15:29:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/06	15:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/06	16:00:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	01	00	00	00	00
2012/02/06	16:02:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/02/06	16:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/02/06	16:02:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2012/02/06	16:02:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2012/02/06	16:02:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/06	16:02:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/06	16:02:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2012/02/06	16:02:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2012/02/06	16:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/06	16:40:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/06	16:40:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da				
2012/02/06	16:40:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2012/02/06	16:43:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2012/02/06	17:04:30.0	XRT_Custom_418_OG [0x1a2]								
2012/02/06	17:05:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/06	17:54:24.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/06	17:54:26.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2012/02/06	17:54:30.0	AOCS_ORe-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00	00	00	00	00
2012/02/06	17:54:46.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2012/02/06	17:54:48.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2012/02/06	17:54:50.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2012/02/06	17:57:28.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2012/02/06	17:57:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2012/02/06	18:04:24.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2012/02/06	18:04:30.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	01	00	00	00	00
2012/02/06	18:06:56.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2012/02/06	18:07:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2012/02/06	18:07:18.0	XRT_FLRCTRL_ENA_413_OG [0x19d]								

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2012/02/06	18:07:20.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_AEC_RESET	1	07-F0	d0
2012/02/06	18:07:22.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/06	18:07:24.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/06	18:07:26.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2012/02/06	18:07:28.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/02/06	18:07:30.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/06	18:18:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/06	18:18:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/06	18:18:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/06	18:21:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/06	18:41:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/06	18:42:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/06	19:55:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/06	19:55:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/06	19:55:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/06	19:58:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/06	20:18:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/06	20:19:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/06	21:32:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/06	21:32:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/06	21:32:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/06	21:35:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/06	21:55:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/06	21:56:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/06	23:09:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/06	23:09:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/06	23:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/06	23:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/06	23:26:00.0	XRT_Custom_418_OG [0x1a2]				
2012/02/06	23:27:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/07	00:44:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/07	00:44:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/07	00:44:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/07	00:47:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/07	00:53:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/07	00:54:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/07	02:09:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/07	02:09:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/07	02:09:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/07	02:12:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/07	02:29:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/07	02:30:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/07	03:38:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/07	03:38:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/07	03:38:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/07	03:41:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/07	04:06:30.0	XRT_Custom_418_OG [0x1a2]				
2012/02/07	04:07:30.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/07	05:17:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/07	05:17:02.0	XRT_FLD_RESET_412_OG [0x19c]				

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2012/02/07	05:17:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/07	05:20:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/07	05:44:00.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/07	05:45:00.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/07	05:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/07	05:59:56.0	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2012/02/07	06:00:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00 00 00 00 00
2012/02/07	06:00:16.0	XRT_FLD_DIS_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9
2012/02/07	06:00:18.0	XRT_FLRCTRL_DIS_433_OG [0x1b1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2012/02/07	06:00:20.0	XRT_ARS_DIS_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/07	06:02:58.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2012/02/07	06:03:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/07	06:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/07	06:10:00.0	AOCS_ORe-point_Start_25_OG [0x0af]	AOCU_NM	5	02-76	00 e2 4e b1 f4
2012/02/07	06:12:26.0	XRT_FOCUS_POSITION_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2012/02/07	06:12:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2012/02/07	06:12:48.0	XRT_FLRCTRL_ENA_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2012/02/07	06:12:50.0	XRT_AEC_RESET_443_OG [0x1bb]	MDP_XRT_AEC_RESET	1	07-F0	d0
2012/02/07	06:12:52.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2012/02/07	06:12:54.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/07	06:12:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2012/02/07	06:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2012/02/07	06:13:00.0	XRT_CTRL_AUTO_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/07	06:57:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/07	06:57:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/07	06:57:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/07	07:00:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/07	07:21:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/07	07:22:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/07	08:37:00.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/07	08:37:02.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/07	08:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/07	08:37:04.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/07	08:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/07	08:40:14.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/07	08:58:00.5	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/07	08:59:00.5	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/07	10:18:30.0	XRT_CTRL_MANU_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/07	10:18:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/07	10:18:32.0	XRT_FLD_RESET_412_OG [0x19c]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/07	10:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/07	10:18:34.0	XRT_PREFLR_STRT_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2012/02/07	10:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2012/02/07	10:21:44.0	XRT_PREFLR_STOP_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/07	10:28:30.0	XRT_Custom_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/07	10:29:30.0	XRT_CTRL_AUTO_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2012/02/07	10:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2012/02/07	10:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2012/02/07	10:51:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00 00 00 00 00